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TO : CHIWEY

Date : 2009/07/23

## Customer Acceptance Specification

Model : HSD043I9W1-A00-0299  
HSD043I9W1-A01-0299

相關文件: IIS

Accepted by:	
Signature	Date
Proposed by: Technical Service Division	
Signature	Date

Note:1. Please contact HannStar Display Corp. before designing your product based on this module specification.

2.The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

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## Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	2009/07/23	A00-0299/ A01-0299	Formal Product Specification was first issued.



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## 1.0 GENERAL DESCRIPTION

### 1.1 Introduction

HannStar Display model HSD043I9W1-A00-0299 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit. This TFT LCD has a 4.3 (16:9) inch diagonally measured active display area with WQVGA (480 horizontal by 272 vertical pixel) resolution.

### 1.2 Features

- 4.3 (16:9 diagonal) inch configuration
- 8-bit color depth with 256 gray-scale
- Parallel 24-bit RGB data input
- RoHS and Halogen-Free compliance

### 1.3 Applications

- Personal Navigation Device
- Multimedia applications and Others AV system

### 1.4 General information

Item	Specification	Unit
Outline Dimension	103.04 x 64.6 x 1.43 (Typ.)	mm
Display area	95.04 (H) x 53.856 (V)	mm
Number of Pixel	480 RGB (H) x 272 (V)	pixels
Pixel pitch	0.198 (H) x 0.198 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display mode	Normally white	

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## 2.0 ABSOLUTE MAXIMUM RATINGS

### 2.1 Electrical Absolute Rating

#### 2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power supply voltage	VDD	-0.3	5.0	V	GND=0
Logic Signal Input Level	V <sub>i</sub>	-0.3	5.0	V	

#### Note

- (1) Permanent damage may occur to the LCD module if beyond this specification.  
Functional operation should be restricted to the conditions described under normal operating conditions.
- (2) Ta =25±2℃

### 2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T <sub>opa</sub>	-20	70	℃	
Storage Temperature	T <sub>stg</sub>	-30	80	℃	

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### 3.0 OPTICAL CHARACTERISTICS

#### 3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Threshold voltage		Vsat		—	2.4	—		(6)	
		Vth		—	1.4	—		(6)	
Transmittance (With PZ)		T		—	6.78	—			
Contrast		CR	Θ=0 Normal viewing angle	480	600	—		(1)(2)	
Response time	Rising	T <sub>R</sub>		—	3	6	msec	(1)(3)	
	Falling	T <sub>F</sub>		—	7	14			
Color gamut		S		—	50	—	%	C light	
Color chromaticity (CIE1931)	White	W <sub>x</sub>		Θ=0 Normal viewing angle	0.292	0.307	0.322		(1)(4) CF Glass C light
		W <sub>y</sub>			0.333	0.348	0.363		
	Red	R <sub>x</sub>			0.616	0.631	0.646		
		R <sub>y</sub>			0.327	0.342	0.357		
	Green	G <sub>x</sub>	0.306		0.321	0.336			
		G <sub>y</sub>	0.538		0.553	0.568			
	Blue	B <sub>x</sub>	0.134		0.149	0.164			
		B <sub>y</sub>	0.168		0.183	0.198			
Viewing angle	Hor.	Θ <sub>L</sub>	CR>10	65	75	—			
		Θ <sub>R</sub>		65	75	—			
	Ver.	Θ <sub>U</sub>		50	60	—			
		Θ <sub>D</sub>		60	70	—			
Optima View Direction		6 O’ clock						(5)	

#### 3.2 Measuring Condition

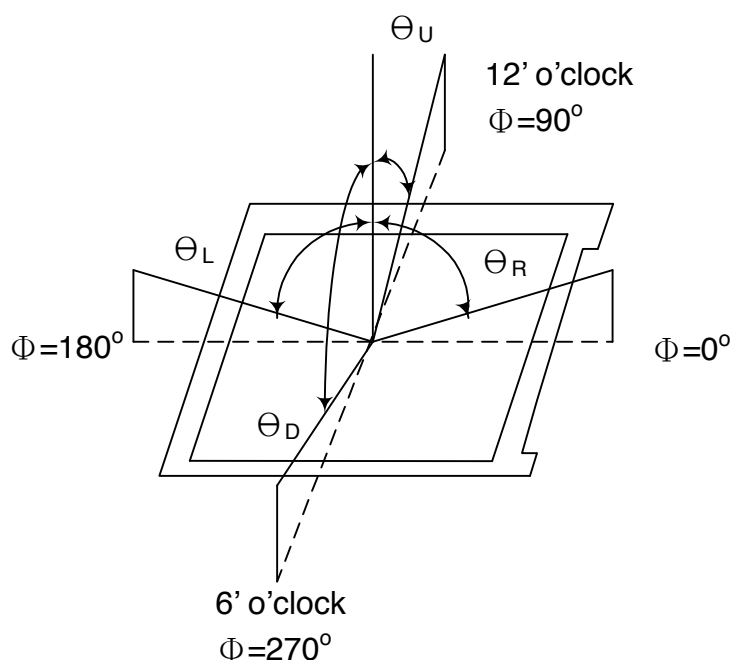
- Measuring surrounding: dark room
- LED current I<sub>L</sub>: 40mA
- Ambient temperature: 25±2°C
- 15min. warm-up time.

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### 3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size: 20 ~ 21 m

Note (1) Definition of Viewing Angle:

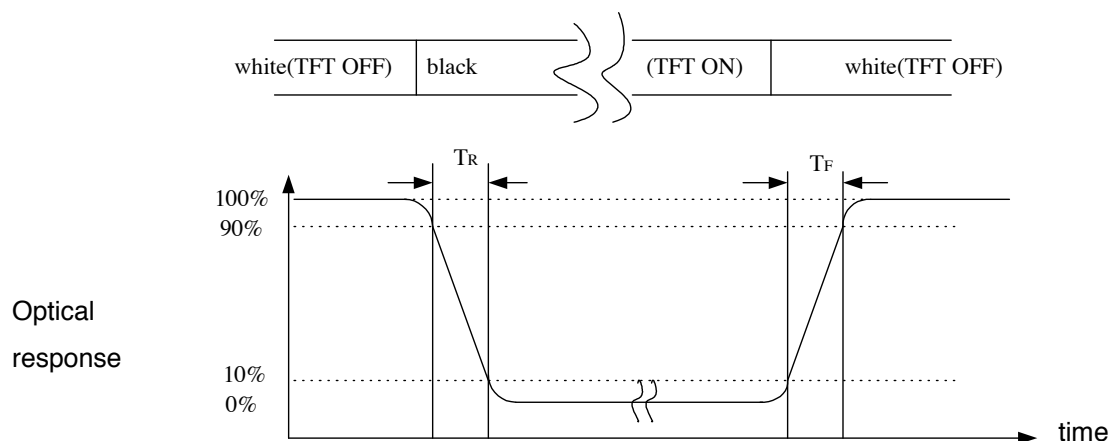


Note (2) Definition of Contrast Ratio (CR):  
Measured at the center point of panel

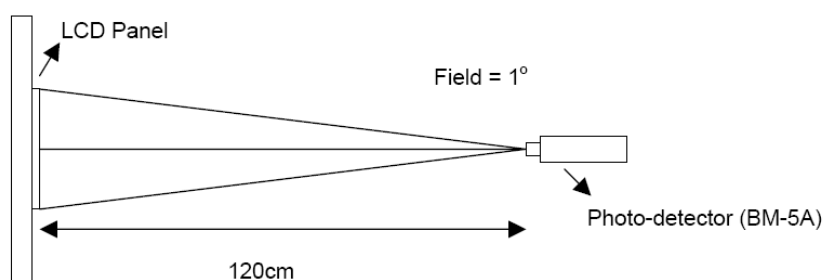
$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

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Note (3) Definition of Response Time: Sum of  $T_R$  and  $T_F$



Note (4) Definition of optical measurement setup

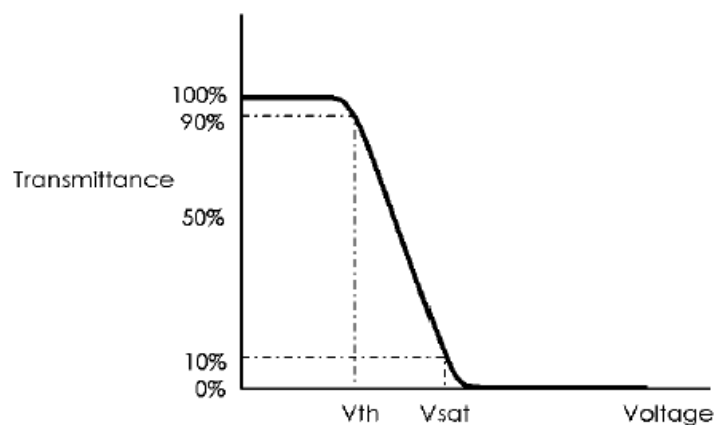




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Note (5) Rubbing Direction (The different Rubbing Direction will cause the different optimal view direction.

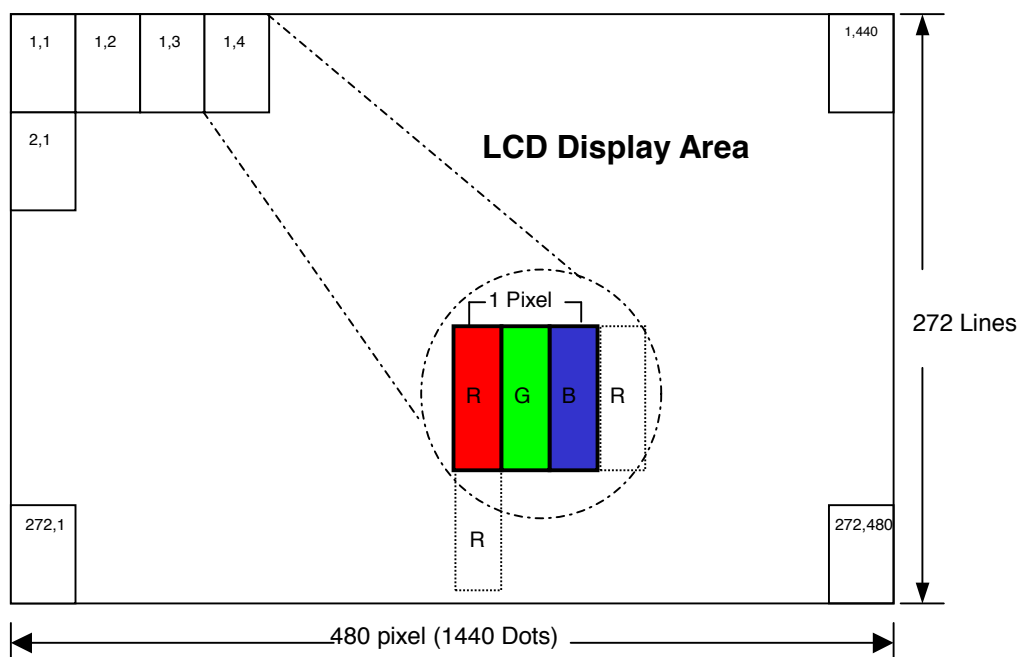
Note (6) Definition of  $V_{sat}$  and  $V_{th}$  (at 20°C)



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## 4.0 BLOCK DIAGRAM

### 4.1 Pixel Format



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## 5.0 INPUT INTERFACE PIN ASSIGNMENT

FPC connector is used for electronics interface. The recommended model is FH19SC-40S-0.5SH (05) manufactured by HIROSE.

Pin No	Symbol	I/O	Function
1	V <sub>LED-</sub>	P	Power for LED backlight cathode
2	V <sub>LED+</sub>	P	Power for LED backlight anode
3	GND	P	Power ground
4	V <sub>DD</sub>	P	Power voltage
5	R0	I	Red data (LSB)
6	R1	I	Red data
7	R2	I	Red data
8	R3	I	Red data
9	R4	I	Red data
10	R5	I	Red data
11	R6	I	Red data
12	R7	I	Red data (MSB)
13	G0	I	Green data (LSB)
14	G1	I	Green data
15	G2	I	Green data
16	G3	I	Green data
17	G4	I	Green data
18	G5	I	Green data
19	G6	I	Green data
20	G7	I	Green data (MSB)
21	B0	I	Blue data (LSB)
22	B1	I	Blue data
23	B2	I	Blue data
24	B3	I	Blue data
25	B4	I	Blue data
26	B5	I	Blue data
27	B6	I	Blue data
28	B7	I	Blue data (MSB)
29	GND	P	Power ground
30	DCLK	I	Pixel clock
31	DISP	I	Display on/ off
32	HSYNC	I	Horizontal sync signal
33	VSYNC	I	Vertical sync signal
34	DE	I	Data enable
35	NC	-	No connect
36	GND	P	Power ground
37	X_R	I/O	Right electrode - differential analog
38	Y_B	I/O	Bottom electrode - differential analog
39	X_L	I/O	Left electrode - differential analog
40	Y_T	I/O	Top electrode - differential analog

I/O: I: input, O: output, P: power

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## 6.0 ELECTRICAL CHARACTERISTICS

### 6.1 DC Electrical Characteristics

Parameters	Symbol	Min.	Typ.	Max.	Unit	Note
Supply voltage	$V_{DD}$	3.0	3.3	3.6	V	
Input signal voltage	$V_{IH}$	$0.7 V_{DD}$	—	$V_{DD}$	V	Note (1)
	$V_{IL}$	GND	—	$0.3 V_{DD}$	V	Note (1)
Current of power supply	$I_{DD}$	—	TBD	—	mA	$V_{DD} = 3.3V$

Note (1): HSYNC, VSYNC, DE, R/G/B Data

Note (2): GND = 0V

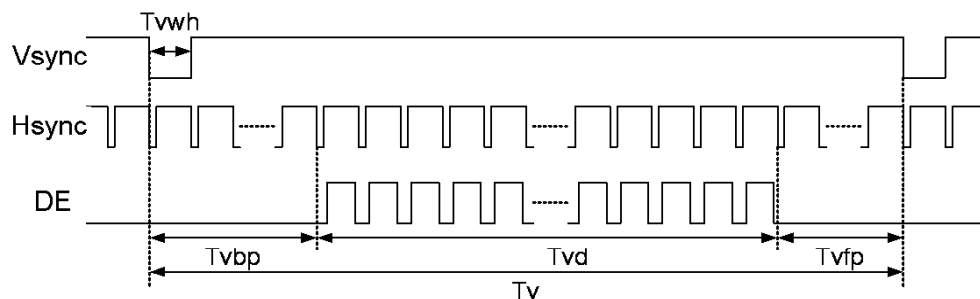
### 6.2 Data Input Format

#### Parallel 24-bit RGB Input Timing Table

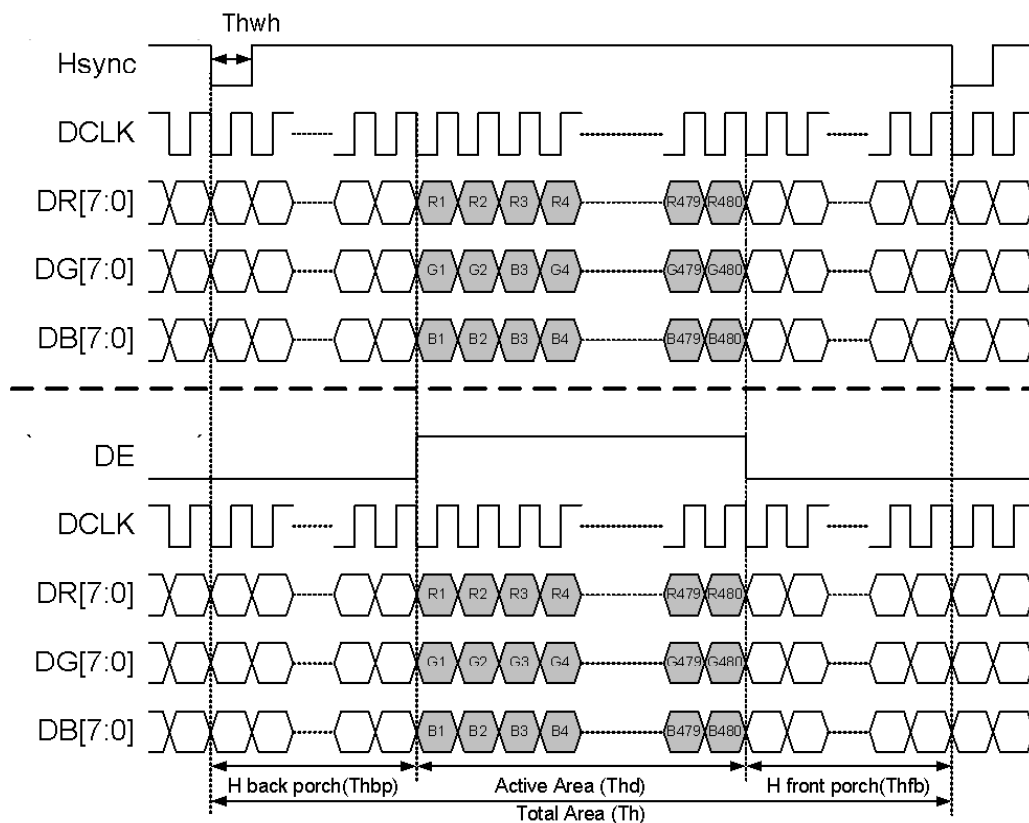
Parameters	Symbol	Min.	Typ.	Max.	Unit	Conditions
DCLK frequency	fclk	5	9	12	MHz	
VSYNC period time	$T_v$	277	288	400	Th	
VSYNC display area	$T_{vd}$	272			Th	
VSYNC back porch	$T_{vbp}$	3	8	31	Th	
VSYNC front porch	$T_{vfp}$	2	8	93	Th	
HSYNC period time	$T_h$	520	525	800	DCLK	
HSYNC display area	$T_{hd}$	480			DCLK	
HSYNC back porch	$T_{hbp}$	36	40	255	DCLK	
HSYNC front porch	$T_{hfp}$	4	5	65	DCLK	

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### Vertical Input Timing



### Parallel 24-bit RGB Mode Data Format (DE Mode)

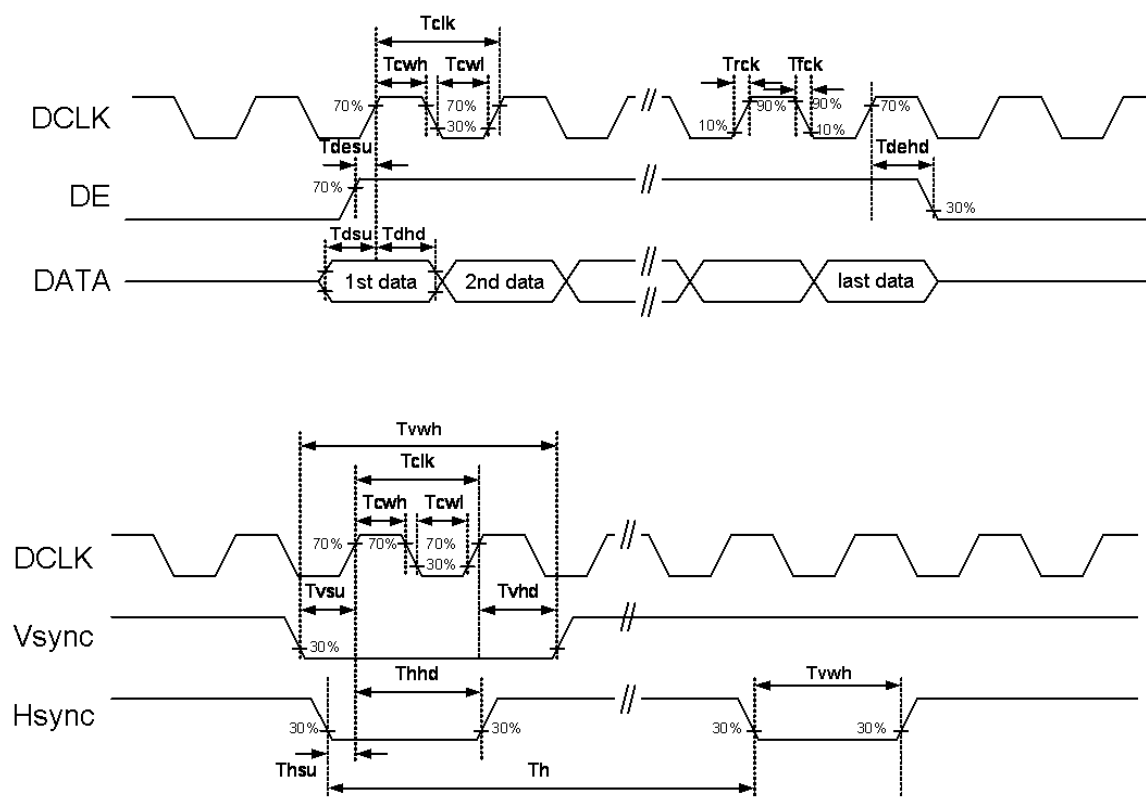


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### 6.3 AC Electrical Characteristics

Parameters	Symbol	Min.	Typ.	Max.	Unit	Conditions
DCLK period time	Tclk	83.3	111.1	200	ns	Parallel 24-bit RGB mode
		33.3	37.0	41.7	ns	Serial 8-bit RGB mode
DCLK rising time	Trck	-	-	9	ns	
DCLK falling time	Tfck	-	-	9	ns	
DCLK pulse duty	Tcwh	40	50	60	%	
DE setup time	Tdesu	12	-	-	ns	
DE hold time	Tdehd	12	-	-	ns	
HSYNC pulse width	Thwh	1	-	-	DCLK	
HSYNC setup time	Thsu	12	-	-	ns	
HSYNC hold time	Thhd	12	-	-	ns	
VSYNC pulse width	Tvwh	1	-	-	Th	
VSYNC setup time	Tvsu	12	-	-	ns	
VSYNC hold time	Tvhd	12	-	-	ns	
Data setup time	Tdsu	12	-	-	ns	
Data hold time	Tdhd	12	-	-	ns	

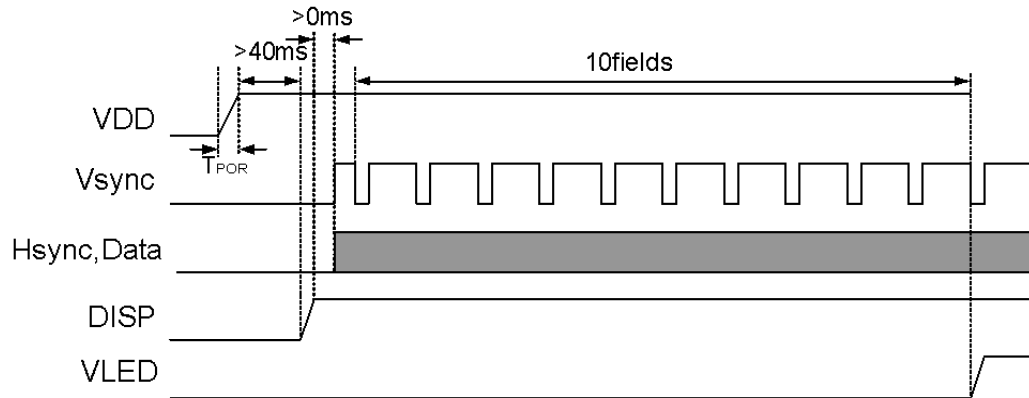
#### Clock and Data Input Timing Diagram



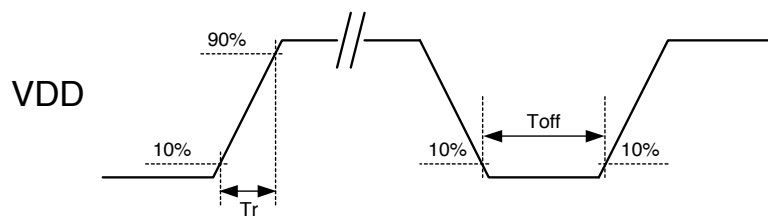
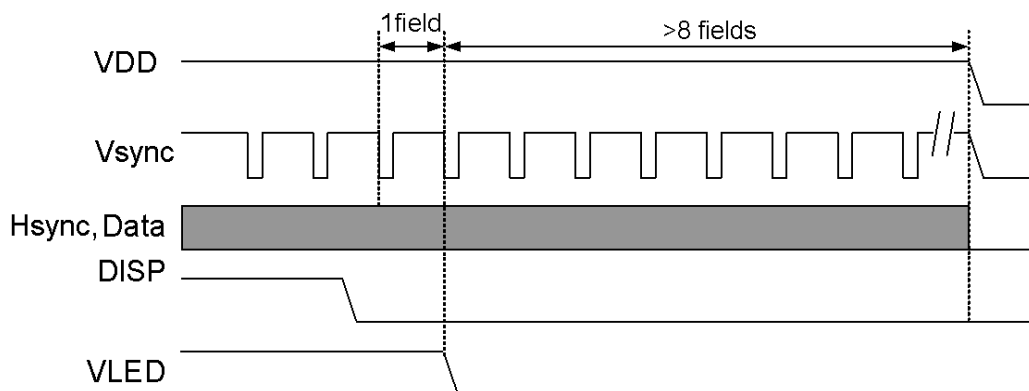
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## 6.4 Power On/Off Sequence

### Power On Sequence



### Power Off Sequence



VDD power input timing

#### Notes:

Data include R0~R7, G0~G7, B0~B7, HSD, VSD, DCLK, DE

Power on sequence: VDD  $\rightarrow$  DISP  $\rightarrow$  Data  $\rightarrow$  V<sub>LED</sub>

Power off sequence: DISP  $\rightarrow$  V<sub>LED</sub>  $\rightarrow$  Data  $\rightarrow$  VDD

VDD power input timing:  $0.5\text{ms} < T_r < 10\text{ms}$ ;  $T_{off} > 500\text{ms}$

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## 7.0 Reliability test items

No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+80°C, 240hrs	
2	Low Temperature Storage	Ta=-30°C, 240hrs	
3	High Temperature Operation	Ta=+70°C, 240hrs	
4	Low Temperature Operation	Ta=-20°C, 240hrs	
5	High Temperature and High Humidity (operation)	Ta=+60°C, 90%RH, 240hrs	
6	Thermal Cycling Test (non operation)	-30°C(30min) → +80°C(30min), 200cycles	
7	Electrostatic Discharge	±200V,200pF(0Ω) 1 time/each terminal	
8	Vibration	1.Random: 1.04Grms, 5~500Hz, X/Y/Z, 30min/each direction 2. Sine: Freq. Range: 8~33.3Hz Stoke: 1.3mm Sweep: 2.9G, 33.3~400Hz X/Z: 2hr, Y: 4hr, cyc: 15min	
9	Shock	100G, 6ms, ±X, ±Y, ±Z 3 time for each direction	JIS C7021, A-10 (Condition A)
10	Vibration (with carton)	Random: 0.015G <sup>2</sup> /Hz, 5~200Hz -6dB/Octave, 200~400Hz XYZ each direction: 2hr	
11	Drop (with carton)	Height: 60cm 1 corner, 3 edges, 6 surfaces	JIS Z0202

Note: There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

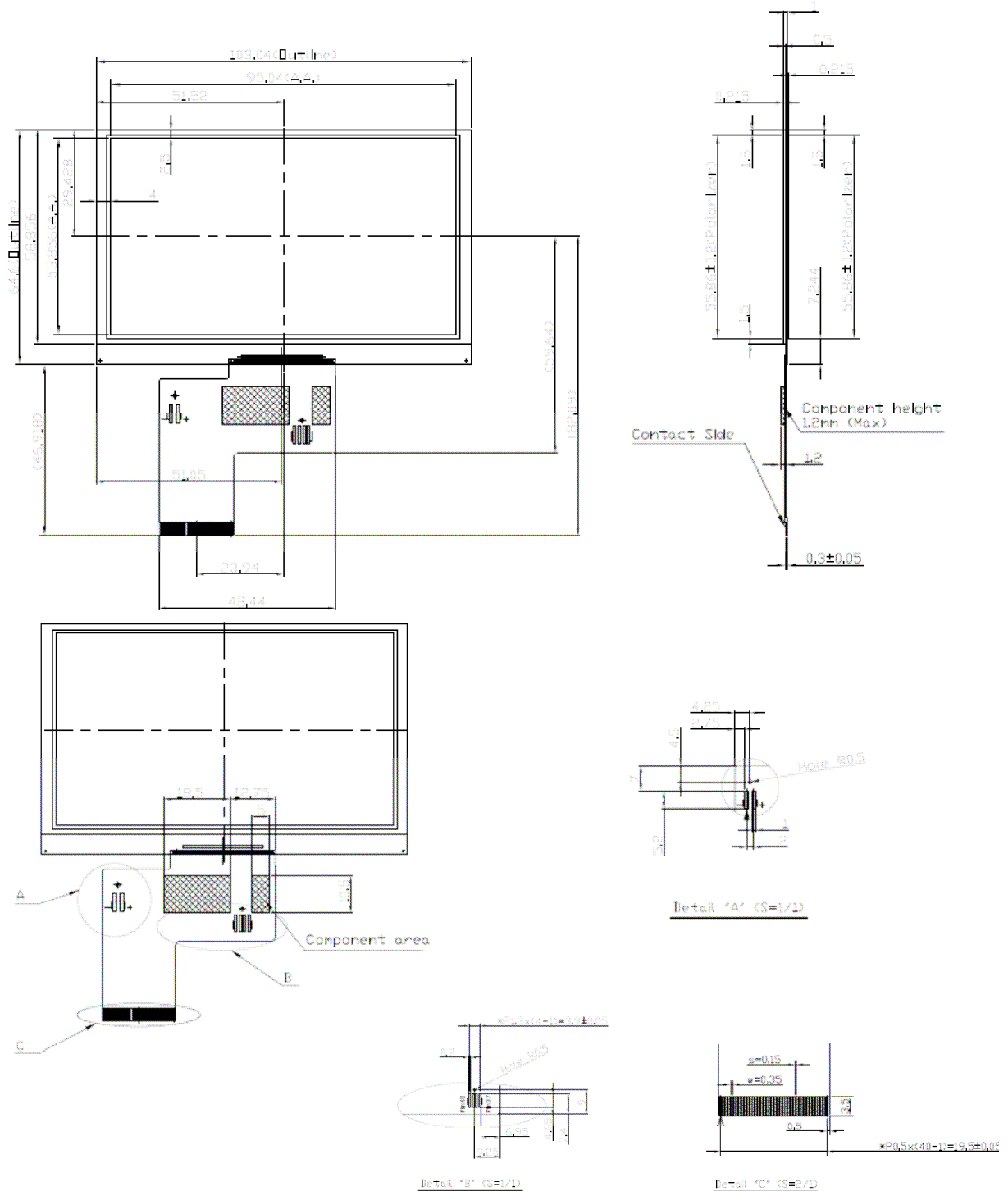


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## 8.0 OUTLINE DIMENSION

### 8.1 Outline Dimension

Unit: mm



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## 9.0 LOT MARK

### 9.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----

code 1,2,3,4,5,6: HannStar internal flow control code.

code 7: production location.

code 8: production year.

code 9: production month.

code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year, for example

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

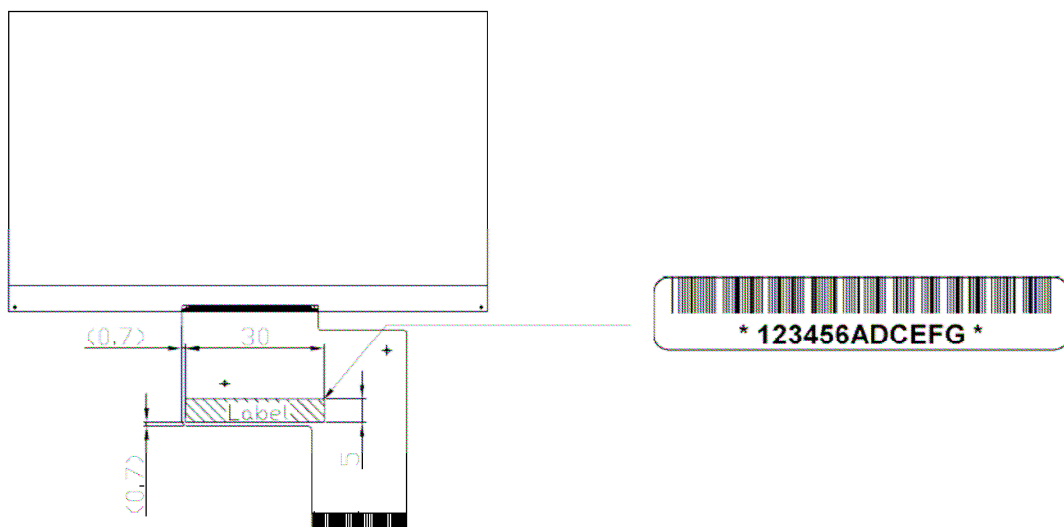
Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

### 9.2 Location of Lot Mark

(1) Detail of the Mark: as attached below.

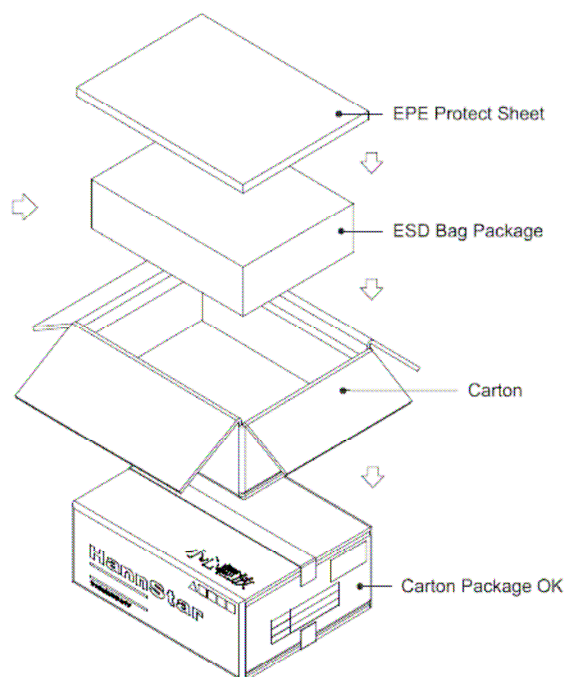
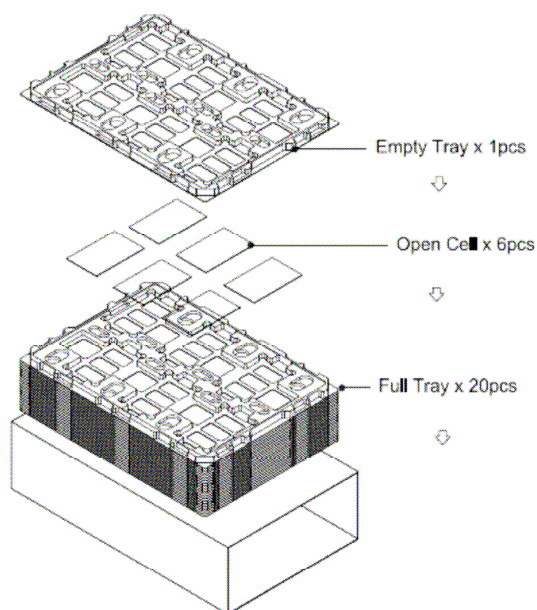
(2) This is subject to change without prior notice.



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## 10.0 PACKAGE SPECIFICATION

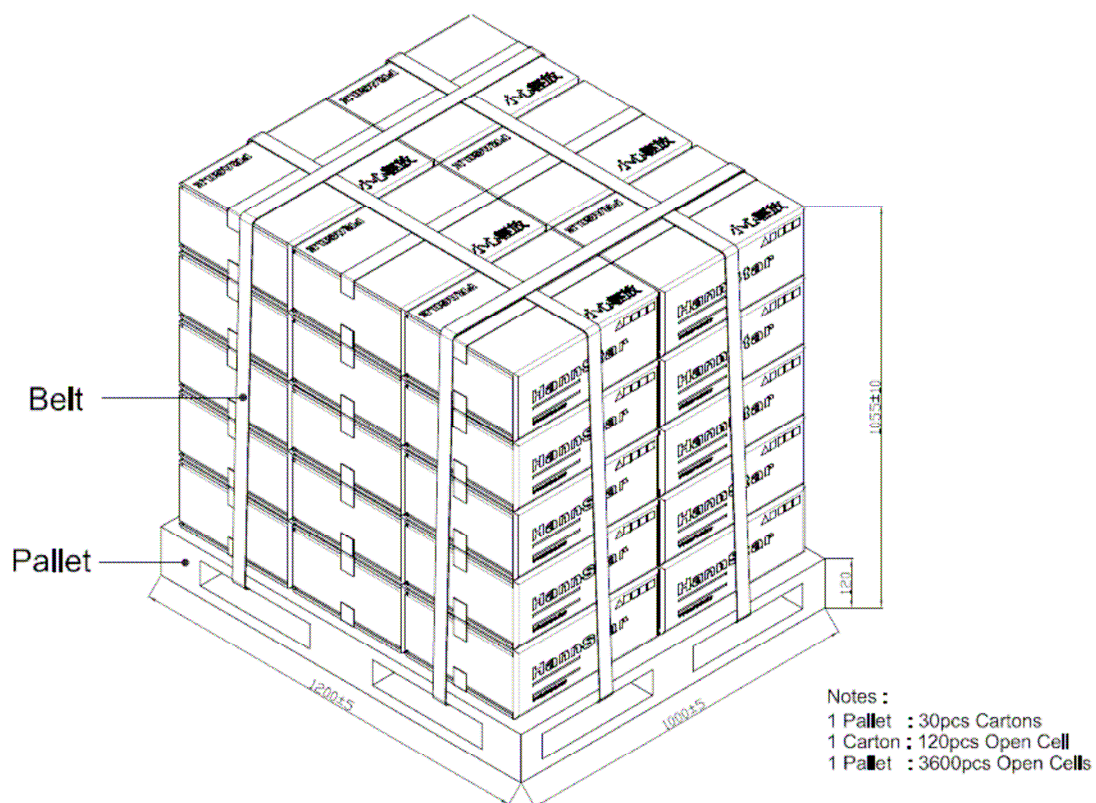
### 10.1 4.3 inch Open Cell Package



1. Tray Stack Quantity : 21pcs
2. MDL to Stack Quantity : 120pcs

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## 10.2 Pallet drawings



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## 11.0 GENERAL PRECAUTION

### 11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

### 11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

### 11.3 Breakage of LCD Panel

- 11.3.1. If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

### 11.4 Electric Shock

- 11.4.1. Disconnect power supply before handling LCD module.
- 11.4.2. Do not pull or fold the LED cable.
- 11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

### 11.5 Absolute Maximum Ratings and Power Protection Circuit

- 11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 11.5.3. It's recommended to employ protection circuit for power supply.

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## 11.6 Operation

- 11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.
- 11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

## 11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

## 11.8 Static Electricity

- 11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 11.8.2. Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

## 11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

## 11.10 Disposal

When disposing LCD module, obey the local environmental regulations.