

LQ104S1DG21

TFT-LCD Module

(Model Number: LQ104S1DG21)

Specifications

Spec No.:LD-14304

Dated: June 10, 2002

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		SPECIFICATION	AVC LIQUID CRYSTAL DISPLAY			
			GROUP			

DEVICE SPECIFICATION FOR

TFT-LCD Module MODEL No. LQ104S1DG21

☐ CUSTOMER'S APPROVAL	
DATE	-
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PRESENTED

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1. Application

This specifications applies to color TFT-LCD module, LQ104S1DG21.

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Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment(trunk lines), nuclear power control equipment and medical or other equipment for life support.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit and power supply circuit and a backlight unit. Graphics and texts can be displayed on a $800 \times 3 \times 600$ dots panel with 262,144 colors by supplying 18 bit data signal (6bit/color), four timing signals, +3.3V or +5V DC supply voltage for TFT-LCD panel driving and supply voltage for backlight.

The TFT-LCD panel used for this module is a low-reflection and higher-color-saturation type. Therefore, this module is also suitable for the multimedia use.

Optimum viewing direction is 6 o'clock.

Backlight-driving DC/AC inverter is not built in this module.



3. Mechanical Specifications

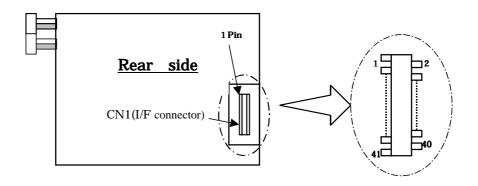
Parameter	Specifications	Unit
Display size	26 (10.4") Diagonal	cm
Active area	211.2(H)×158.4(V)	mm
Pixel format	800(H)×600(V)	pixel
	(1 pixel = R + G + B dots)	
Pixel pitch	$0.264(H) \times 0.264(V)$	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	$246.5(W) \times 179.4(H) \times 15.5 max(D)$	mm
(excluding backlight cables)	Outline dimensions is shown in Fig.1	
Mass	620 max	g
Surface treatment	Anti-glare and hard-coating	

4. Input Terminals

4-1. TFT-LCD panel driving

Connector name : CN1 Used connector:DF9MA-41P-1V (Hirose Electric Co., Ltd.)

Corresponding connector: DF9-41S-1V,DF9A-41S-1V,DF9B-41S-1V,DF9M-41S-1V





Pin No.	Symbol	Function	Remark
1	GND	_	
2	CK	Clock signal for sampling each data signal	_
3	GND	_	_
4	Hsync	Horizontal synchronous signal	[Note1]
5	Vsync	Vertical synchronous signal	[Note1]
6	GND	_	_
7	GND	_	_
8	GND	_	_
9	R0	R E D data signal(LSB)	_
10	R1	RED data signal	_
11	R2	RED data signal	_
12	GND	_	_
13	R3	R E D data signal	_
14	R4	R E D data signal	_
15	R5	R E D data signal(MSB)	_
16	GND	_	_
17	GND	_	_
18	GND	_	_
19	G0	GREEN data signal(LSB)	_
20	G1	GREEN data signal	_
21	G2	GREEN data signal	_
22	GND	_	_
23	G3	GREEN data signal	_
24	G4	GREEN data signal	_
25	G5	GREEN data signal(MSB)	_
26	GND	_	_
27	GND	_	_
28	GND	-	_
29	В0	B L U E data signal(LSB)	_
30	B1	B L U E data signal	_
31	B2	B L U E data signal	_
32	GND		_
33	В3	BLUE data signal	_
34	B4	B L U E data signal	_
35	B5	B L U E data signal(MSB)	_
36	GND	_	_
37	ENAB	Signal to settle the horizontal display position	[Note2]
38	R/L	Horizontal display mode select signal	
39	Vcc	power supply (+3.3Vor+5.0V)	_
40	Vcc	power supply (+3.3Vor+5.0V)	_
41	U/D	Vertical display mode select signal	_

X The shielding case is connected with GND.

[[]Note1] The polarity of both synchronous signals are negative.

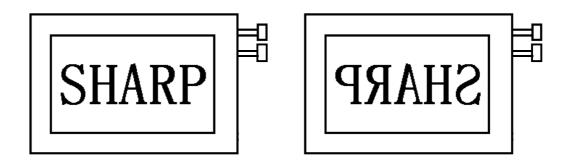
[[]Note2] The horizontal display start timing is settled in accordance with a rising timing of ENAB signal. In case ENAB is fixed "Low", the horizontal start timing is determined as described in 7-2. Don't keep ENAB "High" during operation.



[Note3] R/L=High, U/D=Low

R/L=Low, U/D=Low

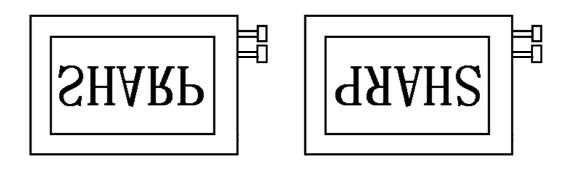
Please do not use this terminal by "open".



[Note4] R/L=High, U/D=High

R/L=Low, U/D=High

Please do not use this terminal by "open".



4-2. Backlight driving

Connector name: CN2, CN3 Used connector: BHR-03VS-1(JST)

Corresponding connector :SM02(8.0)B-BHS(JST)

		1 8	_ ` /
Pin no.	Symbol	Function	Cable color
1	V_{HIGH}	Power supply for lamp (High voltage side)	Pink
2	NC	This is electrically opened.	
3	V_{LOW}	Power supply for lamp (Low voltage side)	White

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	$V_{\rm I}$	Ta=25°C	$-0.3 \sim +5.5$	V	[Note1]
+5V supply voltage	Vcc	Ta=25°C	$0 \sim + 6$	V	
Storage temperature	Tstg	_	-35~70	$^{\circ}\! \mathbb{C}$	
Operating temperature (Ambient)	Topa	_	-10~65	$^{\circ}\!\mathbb{C}$	[Note2]

[Note1] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB, R/L, U/L

[Note2] Humidity: 95%RH Max. at Ta ≤ 50 °C.

Maximum wet-bulb temperature at 39°C or less at Ta>50°C. (No condensation.)

[Note3] Humidity: 95%RH Max. at Ta ≤ 40 °C.

Maximum wet-bulb temperature at 39° C or less at Ta> 40° C. (No condensation.)



6. Electrical Characteristics

6-1.TFT-LCDpaneldriving

 $Ta=25^{\circ}C$

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Power	Supply voltage	Vcc	+3.0	+3.3 +5.0	+5.5	V	[Note1]
Supply	Current dissipation	Icc	1	310	450	m A	Vcc=3.3V [Note2]
		Icc	ı	330	470	m A	Vcc=5.0V [Note2]
Permi	ssive input ripple voltage	V_{RF}	I	_	100	mVp-p	Vcc=5.0V
Input	voltage (Low)	V_{IL}	I	_	0.9	V	[Note3]
Input	voltage (High)	V_{IH}	2.4	_	_	V	
Inp	ut current (low)	I_{OL1}	_	_	1.0	μΑ	V _I =0V [Note4]
		I _{OL2}	-	_	60.0	μΑ	V _I =0V [Note5]
Inp	ut current (High)	I_{OH1}	_	_	1.0	μΑ	V _I =Vcc [Note6]
		I _{OH2}	_	_	60.0	μΑ	V _I =Vcc [Note7]

VCC

3.0V

[NOTE 1]

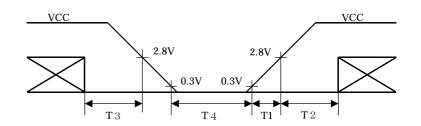
Vcc-turn-on conditions

$$T 1 \le 1 5 m s$$

$$0 < T 2 \le 1 0 m s$$

$$0 < T 3 \le 1 s$$

$$1 s < T 4$$



Vcc-dip conditions

1) 2.
$$7 \text{ V} \le \text{V c c} < 3$$
. 0 V
t $d \le 1 \text{ 0 m s}$

2) V c c < 2.7 V

Vcc-dip condition should also follow

The Vcc-turn-on conditions



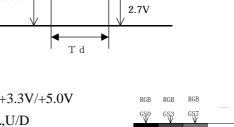
[Note3] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB,R/L,U/D

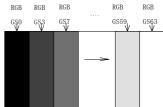
[Note4] CK,R0~R5,G0~G5,B0~B5,Hsync,Vsync,ENAB

[Note5] R/L

[Note6] CK,R0~R5,G0~G5,B0~B5,Hsnc,Vsync

[Note7] ENAB,U/D







6-2. Backlight driving

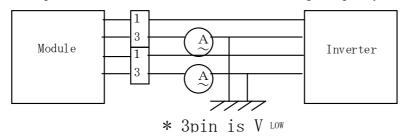
The backlight system is an edge-lighting type with twin CCFT (Cold Cathode Fluorescent Tube).

The characteristics of single lamp are shown in the following table.

Ta=25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Rema	ark
Lamp current	Iμ	3.5	6.0	7.0	mArms	Note	e1]
Lamp power consumption	$P_{\rm L}$	_	2.8	_	W	[Note2]	
Lamp frequency	FL	40	60	70	KHz	[Note	e3】
Kick-off voltage	Vs	ı	_	1000	Vrms	Ta=25°C	
		_	_	1300	Vrms	Ta =0°C	[Note4]
		_	_	1450	Vrms	Ta =-10°C	
Lamp life time	Ll	50000	_	_	hour	[Note5] IL=6.0mA	
	LL	30000	_	_	hour	[Note5]	IL=7.0mA

[Note1] Lamp current is measured with current meter for high frequency as shown below.



- [Note2] At the condition of $Y_1 = 350 \text{cd/m}^2$
- [Note3] Lamp frequency may produce interference with horizontal synchronous frequency, and this may cause beat on the display. Therefore lamp frequency shall be detached as much as possible from the horizontal synchronous frequency and from the harmonics of horizontal synchronous to avoid interference.
- [Note4] The open output voltage of the inverter shall be maintained for more than 1sec; otherwise the lamp may not be turned on.
- [Note5] Since lamp is consumables, the life time written above is referential value and it is not guaranteed in this specification sheet by SHARP.

Lamp life time is defined that it applied either ① or ② under this condition.

(Continuous turning on at Ta=25°C, IL=6 or 7mArms)

- ① Brightness becomes 50% of the original value under standard condition.
- ② Kick-off voltage at Ta= -10°C exceeds maximum value, 1450 Vrms.

In case of operating under lower temp environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating under for around 1 month under lower temp condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp environment, periodical lamp exchange is recommended.

- [Note6] The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. when you confirm it, the module should be operated in the same condition as it is installed in your instrument.
- [Note7] It is required to have the inverter designed so that to allow the impedance deviation of the two CCFT lamps and the capacity deviation of barast capacitor.



7. Timing Characteristics of input signals

Timing diagrams of input signal are shown in Fig.2.

7-1. Timing characteristics

	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc	_	- 40.0		MHz	_
	High time	Tch	6	_	_	ns	_
	Low time	Tcl	6	_	_	ns	_
	Duty ratio	Th/T	40	50	60	%	_
Data	Setup time	Tds	3	_	_	ns	_
	Hold time	Tdh	5	_	_	ns	_
Horizontal	Cycle	TH	20.8	26.4	_	μ s	_
sync. signal			832	1056	_	clock	_
	Pulse width	ТНр	2	128	200	clock	_
Vertical	Cycle	TV	628	666	798	line	_
sync. signal	Pulse width	TVp	2	4	6	line	
Horizontal di	splay period	THd	800	800	800	clock	_
Hsync-Clock	phase difference	ТНс	0	_	Tc-10	ns	_
Hsync-Vsync	c phase difference	TVh	0	_	ТН-ТНр	ns	_
Vertical data	start position	TVs	23	23	23	line	_

Note) In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding to the

rising edge of ENAB signal is displayed at the left end of the active area.

F	Parameter	symbol	Min.	Тур.	Max.	Unit	Remark
Enable signal	Setup time	Tes	5		Tc-10	ns	_
	Pulse width	Тер	2	800	TH-10	clock	-
Hsync-Enable s	signal phase difference	ТНе	58	88	170	clock	

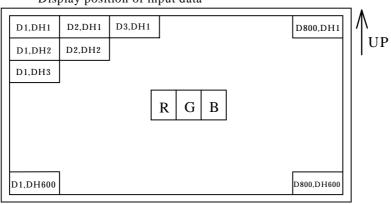
Note) When ENAB is fixed "Low", the display starts from the data of C88(clock) as shown in Fig.2.

7-3. Vertical display position

The vertical display position, TVs is fixed "23" (line).

7-4. Input Data Signals and Display Position on the screen

Display position of input data



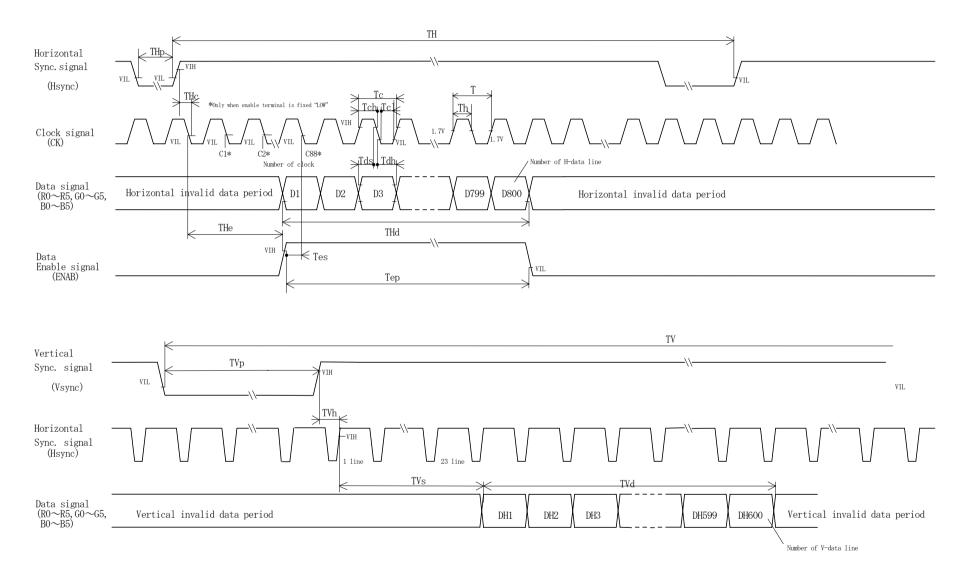


Fig. 2 Input signal waveforms



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

O. Imp	nput Signals, Basic Display Colors and Gray Scale of Each Color																			
	Colors &										a sign	al								
	Gray scale	Gray	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	В0	B1	B2	В3	B4	B5
		Scale																		
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Basic	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
ic (Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Color	Red	_	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
"	Magenta	_	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
y So	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	仓	V			1	<u> </u>					1	<u> </u>					`	ν <u> </u>		
of	Û	V	V					\downarrow					V							
Red	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray	仓	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
7 Sc	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
Scale	仓	\downarrow			1	<u> </u>					1	/					`	V		
of	Û	\downarrow			1	<u> </u>					1	<u> </u>						ν		
Green	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
'n	Û	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gra	仓	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
y Sc	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale	仓	û V					1	\						l						
of	Û	\rightarrow			1	\					1	\						V		
Blue	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
0	Û	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

0 :Low level voltage, 1 : High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.



9. Optical Characteristics

Ta= 25° C, Vcc=+3.3V or +5V

Par	amete	er	Symbol	Condition	Min	Тур	Max	Unit	Remark
Viewing	Н	orizontal	θ 21, θ 22	CR≧10	60	70	_	Deg.	[Note1,4]
Angle	Ţ	/ertical	θ 11		35	40	_	Deg.	
Range			θ 12		55	70	_	Deg.	
Contrast rat	io		CR	$\theta=0^{\circ}$	150	_	_	_	[Note2,4]
				Best Viewing	_	300	_	_	
				Angle					
Response		Rise	τr	$\theta = 0^{\circ}$	_	20	_	m s	[Note3,4]
Time		Decay	τd		-	40	_	m s	
Chromatici	ty of V	White	X		-	0.313	_		[Note4]
			У			0.329			
Luminance	of wh	nite	ΥL		280	350	_	cd/m ²	
White Unif	omity		δ w		-	-	1.45	_	[Note5]
Viewing An	gle	Horizontal	θ 21, θ 22	50% of the	_	35	_	Deg.	[Note1]
range as a		Vertical	θ 11	maximum brightness	_	25	_	Deg.	(Reference value)
Definition Definition	Brightness		θ 12	brightness		30	_	Deg.	

%The measurement shall be executed 30 minutes after lighting at rating. (typical condition:I₁=6.0mArms) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.3 below.

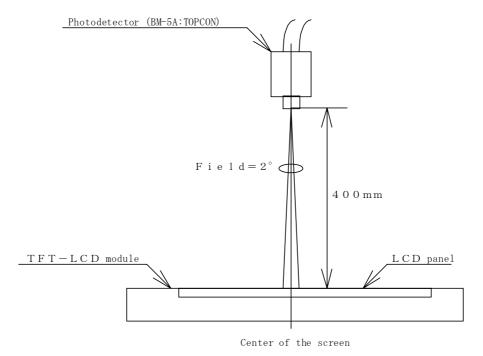
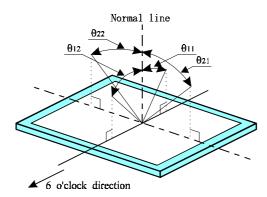


Fig. 3 Optical characteristics measurement method



[Note1] Definitions of viewing angle range:



[Note2] Definition of contrast ratio:

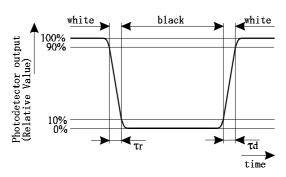
The contrast ratio is defined as the following.

Contrast Ratio (CR) = Luminance (brightness) with all pixels white

Luminance (brightness) with all pixels black

[Note3] Definition of response time:

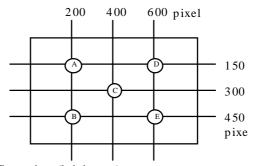
The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.

[Note5] Definition of white uniformity:

White uniformity is defined as the following with five measurements $(A \sim E)$.



 $\delta \text{ w} = \frac{\text{Maximum Luminance of five points (brightness)}}{\text{Minimum Luminance of five points (brightness)}}$



10. Display Quantity

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

11. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarizer is easily damaged, pay attention not to scratch it.
- d) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling.
- h) Observe all other precautionary requirements in handling components.
- i) This module has its circuitry PCBs on the rear side and should be carefully handled in order not to be stressed.
- j) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD. Be careful about the optical interface fringe etc. Which degrades display quality.
- k) Connect GND to 4 place of mounting holes to stabilize against EMI and external noise.
- l) There are high voltage portions on the backlight and very dangerous. Careless touch may lead to electrical shock. When exchange lamps or service. Turn off the power without tail.
- m) Be sure not to apply tensile stress to the lamp lead cable.
- n)Cold cathode fluorescent lamp in LCD panel contains a small amount of mercury, please follow local ordinances or regulations for disposal.

12.Packing form

Piling number of cartons	5 (Max)
Packing quantity in one carton	20
Carton size [mm]	494(W)×326(D)×433(H)
Total mass of one carton filled	15.6kg
with full modules	



13. Reliability test items

	•	
No.	Test item	Conditions
1	High temperature	Ta= 50°C ; 95%RH 240h
	& high humidity storage test	(No condensation)
2	High temperature storage test	Ta= 70°C 240h
3	Low temperature storage test	Ta= -35°C 240h
4	High temperature	Ta= 40°C ; 95%RH 240h
	& high humidity operation test	(No condensation)
5	High temperature operation test	Ta= 65°C 240h
6	Low temperature operation test	Ta= -10°C 240h
7	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.075mm
	(non- operating)	: 58~500Hz/Gravity:9.8m/s ²
		Sweep time: 11 minutes
		Test period : 3 hours
		(1 hour for each direction of X,Y,Z)
8	Shock test	Max. gravity : 490m/s ²
	(non- operating)	Pulse width: 11ms, half sine wave
		Direction: $\pm X, \pm Y, \pm Z$
		once for each direction.

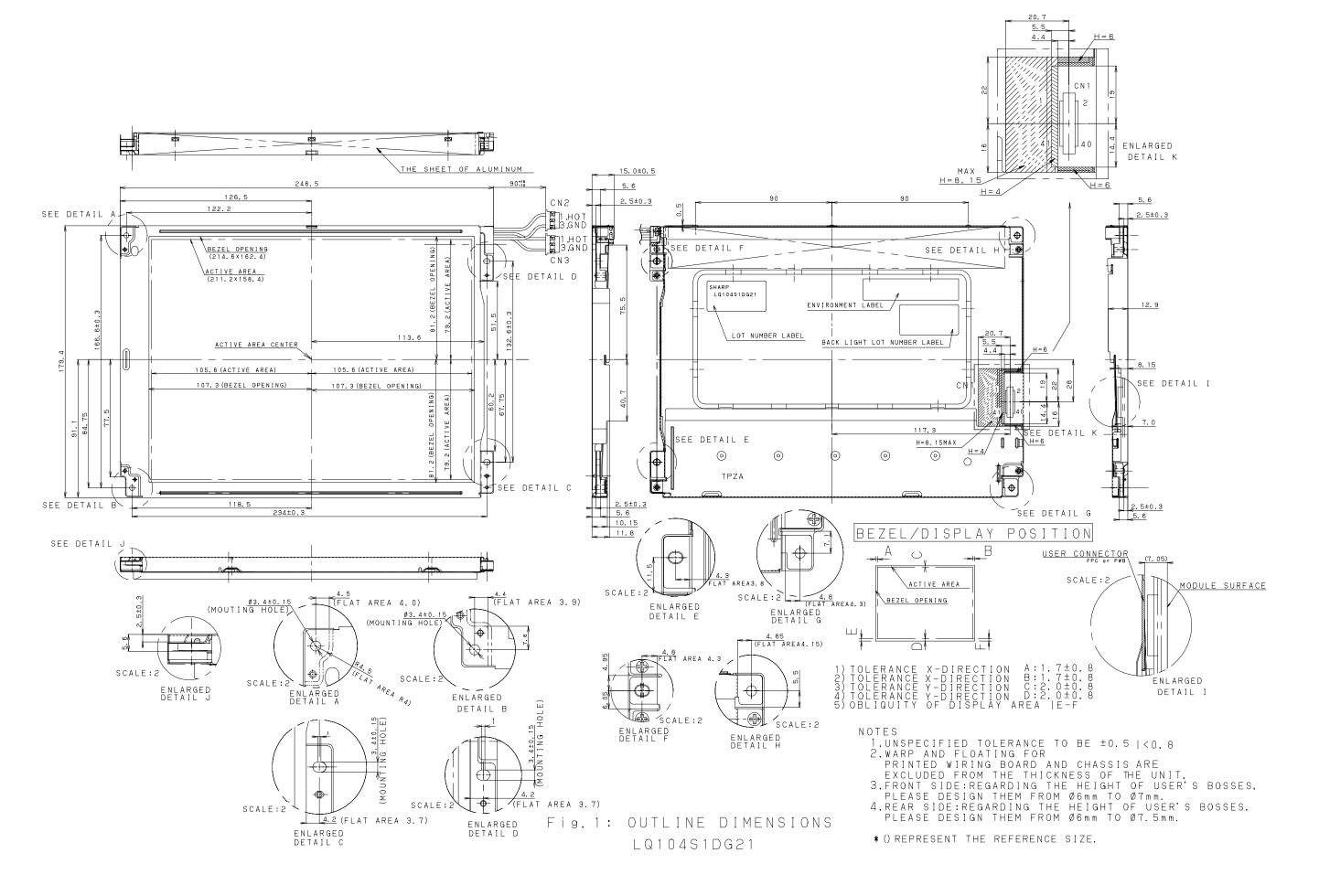
[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

14. Others



- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) Do not use LCD module in the atmosphere of corrosive gases, such as sulfide gas or chlorine gases. Polarizer may deteriorated or cause chemical reaction that can lead to short circuits at the terminal Points. Do not use the material, which compounds contain sulfide or chlorine articles in the vicinity of LCD module. At high temperature, these compounds produce corrosive gases.
- 6) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.



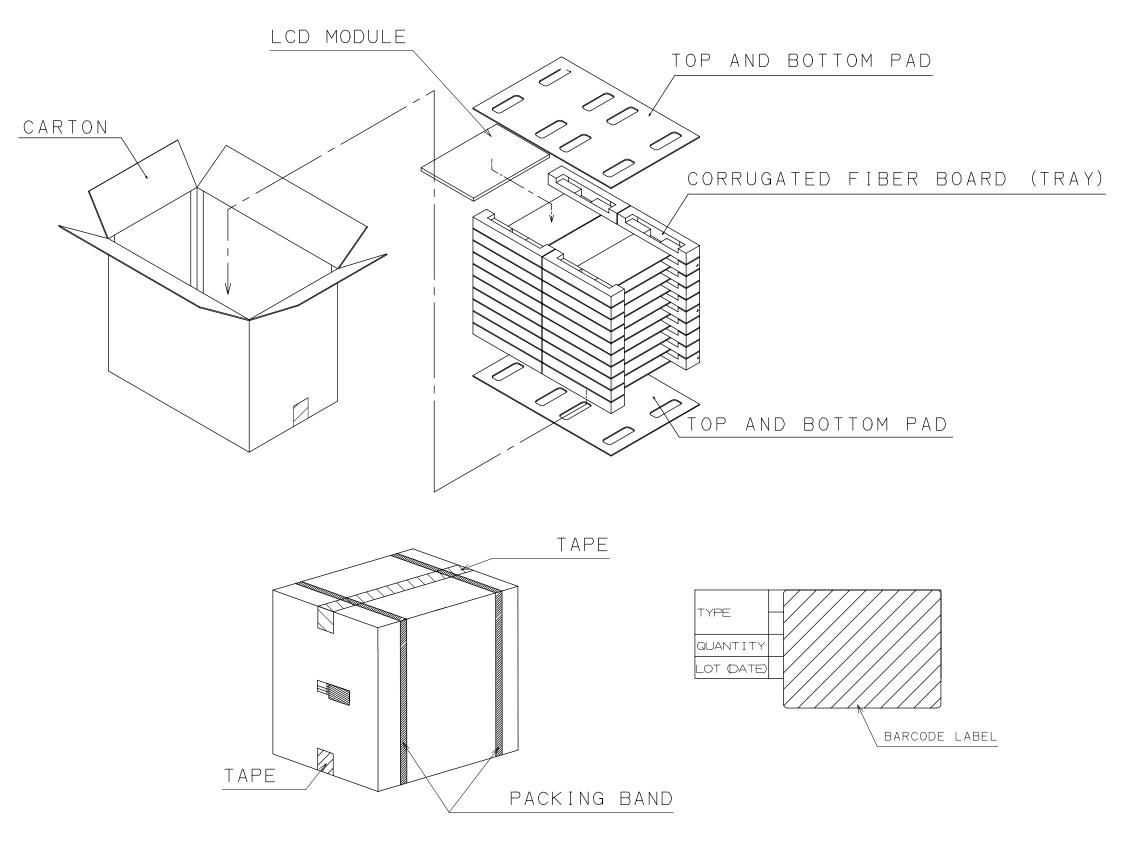


FIG. 3:PACKING FORM

SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.

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