

3.5"-10.1" LCM&TFT Module manufacturer

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SPECIFICATION SHEET

4" STANDARD TFT LCD MODULE

- Model No.:SAT-630-BZ040D-PO-Y

- System: PAL/NTSC Automatic

1. INTRODUCTION

SAT-630-BZ040D-PO-Y TFT module is combined with SAT-630-035-54DS-C PCB board and SAT040HS54DHYO-C0 LCD digital panel, support both PAL system and NTSC ,which can be automatically converted. The whole module use high light white LED as back-light, which has low consumption and disturbances. This TFT module can be used for visual doorbell, video telephony, automotive displays, portable DVD, instruments, meters and measuring equipment and etc.

2. MAIN PARAMETERS

TFT Brand	Hanstar digital panel
Resolution	320RGBX240 dot
Valid display area	82.08(H)*61.56(V)
Color style	R.G.B. Beeline array
Aspect ratio	4:3

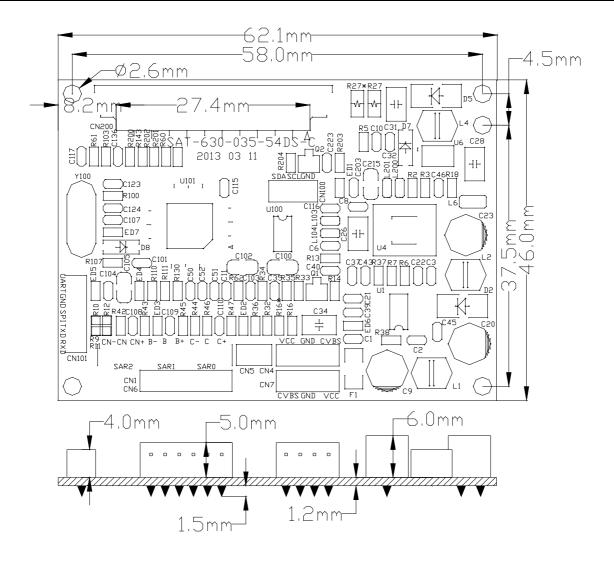
Working voltage	DC9-18V (standard: DC12V)	
Working current (DC12V)	DC140mA±10mA	
Signal system	PAL/NTSC	
Scope of video signal input	standard value: 1.0 Vp-p min value: 0.5 Vp-p max value: 2.0 Vp-p	
Startup time	≤1.8s	
Power consumption	≤1.5W	
Working temperature scope	-40℃-60℃	
Storage temperature scope	-40℃-70℃	

3. PRODUCT PICTURE(JUST FOR REFERENCE)



4. DIGRAM AND INTERFACE DEFINITION

4.1 PCB DIGRAM



4.2 INTERFACE DEFINITION

CN2: 4-wire leads out (PH specifications, 2.0mm space between) for power and video.

Pin	Symbol	Definitions
1	VCC	9V~18V Power Input
2	GND	Power ground
3	GND	Video ground
4	CVBS1	PAL/NTSC VIDEO Input

CN1: 6-wire leads out (PH specifications, 2.0mm space between),

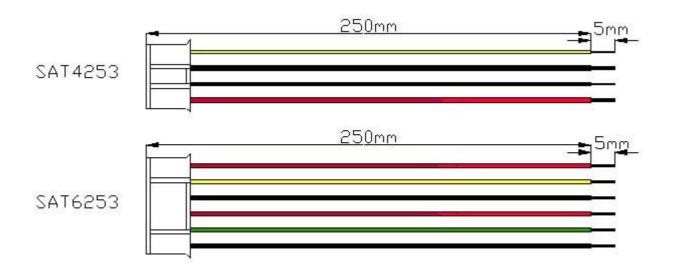
used for potentiometer

Pin	Symbol	Definitions	Resistance
1	COL-	Chrominance VR (-)	1050K

2	COL	Chrominance VR	
3	COL+	Chrominance VR(+)	
4	BRT-	Brightness VR(-)	10 501/
5	BRT	Brightness VR	10—50K
6	BRT+	Brightness VR(+)	

4.3 STANDARD WIRING

SAT4253 SAT6253 cable (UL1007-26#) .



5. PACKING, TRANSPORT AND STORAGE

5.1 PACKING

Sample packing dimension: 120×120×40mm

Module cable: SAT4253-250mm, SAT6253-250mm each 1pc

Large box of packing specifications: 430(L) ×340(W) ×270(H) mm

Carton quantity: Thick brackets :48pcs

Thin brackets:60pcs

each separated by paper card

5.2 TRANSPORT AND STORAGE

To avoid transport during rain or snow days. Prohibit to store with chemical materials and wet

things.

6. ATTENTION

☆The input voltage should not higher than the limitation voltage.

☆Distinguish the position of power line and video line, if connect contract, the board can be

burn out easily.

★The board is electronic product, so static electricity should be precaution during process.

assembling, operate.

☆Panel is glass product, it should be careful when taken and put down, avoid breakage.

★The PFC line connect between panel and PCB connect line, it is easily being break off or pull

apart, so should be careful during process, assembling, operate.

☆when using, pay attention to the input impedance match of signal, on PCB board, the position

of R18* 75 ohm is signal input impedance matching resistance when exist this resistance, it is 75

ohm signal input, when not, it is 50K high resistance input.

7. PANEL DECISION CRITERIA

7.1 TESTING CONDITION

Test distance: 35CM±5CM

View angle: Check when light on: ±5 degree;

Outlook checking: ±45 degree. (from screen surface 0 degrees away in

vertical)

7.2 CIRCUMTANCES

Temp.: 23±5 degree

Humidity: 55±10 degree

Brightness: Outlook check:600LUX;

Lighting on: 300-500LUX

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7.3 INSPECTION METHOD

Scratch: Inspect at 600LUX, from 30CM away, check in vertical (or left/right 45 degree), if no any scratches, then qualified.

Black dot: Compare LCD black dot with Dot investigation standard by eyes.

White/color dot: Cover black dot investigation standard paper on the white dot or color dot, to check if the white/color dot is available.

7.4 INSPECTION STANDARD

Remark: 1. Diameter= (maximum+ minimum)/2

2. Total quantity of Black dot, White dot, Color dot: A+B≤4

DIAMETER (mm)		ALLOWED AREA	
		A area	B area
	d≤0.2	Neglect	Neglect
Black	0.2 <d≤0.3< td=""><td>4</td><td>4</td></d≤0.3<>	4	4
dot	0.3 <d≤0.5< th=""><th>2</th><th>3</th></d≤0.5<>	2	3
	0.5d>0.8	0	2
	d≤0.2	Neglect	Neglect
White	0.2 <d≤0.3< th=""><th>3</th><th>3</th></d≤0.3<>	3	3
dot	0.3 <d≤0.5< th=""><th>1</th><th>2</th></d≤0.5<>	1	2
	0.5d>0.8	0	1

7.5 DIVISION OF A/B AREA

