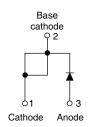


Vishay Semiconductors

## Schottky Rectifier, 8 A

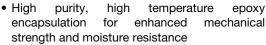




PRODUCT SUMMARY					
Package	TO-220AC				
I <sub>F(AV)</sub>	8 A				
$V_R$	60 V, 80 V, 100 V				
V <sub>F</sub> at I <sub>F</sub>	0.58 V				
I <sub>RM</sub> max.	7 mA at 125 °C				
T <sub>J</sub> max.	175 °C				
Diode variation	Single die				
E <sub>AS</sub>	7.5 mJ				

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation





- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Designed and qualified according to JEDEC-JESD47
- Halogen-free according to IEC 61249-2-21 definition (-N3 only)

#### **DESCRIPTION**

The VS-8TQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	VALUES	UNITS			
I <sub>F(AV)</sub>	Rectangular waveform	8	А			
$V_{RRM}$	Range	60 to 100	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	850	Α			
V <sub>F</sub>	8 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.58	V			
T <sub>J</sub>	Range	- 55 to 175	°C			

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS- 8TQ060PbF	VS- 8TQ060-N3	VS- 8TQ080PbF	VS- 8TQ080-N3	VS- 8TQ100PbF	VS- 8TQ100-N3	UNITS
Maximum DC reverse voltage	$V_R$							
Maximum working peak reverse voltage	V <sub>RWM</sub>	60	60	80	80	100	100	V

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS		
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 157 °C, rectangular waveform		8	Α		
Maximum peak one cycle non-repetitive surge current	1	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	850	Α		
See fig. 7	I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	230	А		
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 0.50 A, L = 60 mH		7.50	mJ		
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		0.50	Α		



# VS-8TQ...PbF Series, VS-8TQ...-N3 Series

## Vishay Semiconductors

ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CO	NDITIONS	VALUES	UNITS		
		8 A	T 05 °C	0.72	V		
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	16 A	T <sub>J</sub> = 25 °C	0.88			
		8 A	T 105 00	0.58			
		16 A	T <sub>J</sub> = 125 °C	0.69			
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = rated V <sub>R</sub>	0.55	mA		
See fig. 2	'RM '''	T <sub>J</sub> = 125 °C	VR = rated VR	7	IIIA		
Maximum junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz) 25 °C		500	pF		
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		8	nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs		

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C			
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	2.0	°C/W			
Typical thermal resistance, case to heatsink	R <sub>thCS</sub>	R <sub>thCS</sub> Mounting surface, smooth and greased		C/VV			
Approximate weight			2	g			
Approximate weight			0.07	OZ.			
Mounting torque minimum			6 (5)	kgf · cm			
Mounting torque maximum			12 (10)	(lbf · in)			
			8TC	060			
Marking device		Case style TO-220AC	8TQ080				
			8TC	100			

## Vishay Semiconductors

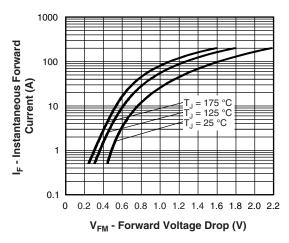


Fig. 1 - Maximum Forward Voltage Drop Characteristics

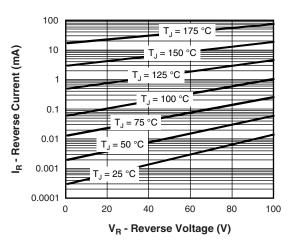


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

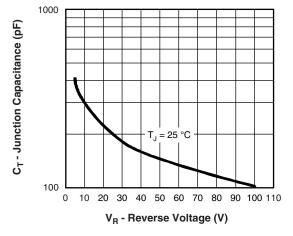


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

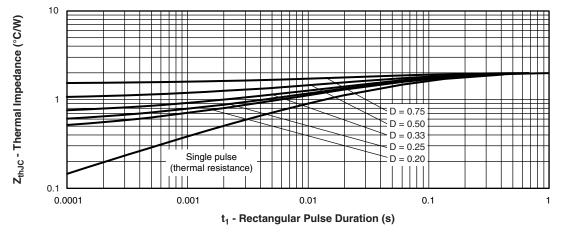


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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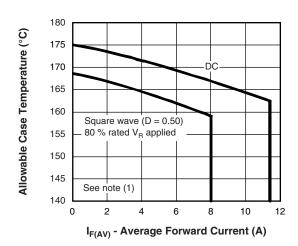


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

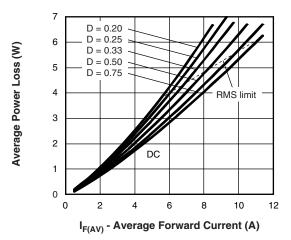


Fig. 6 - Forward Power Loss Characteristics

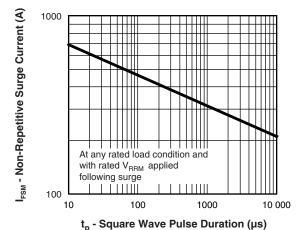


Fig. 7 - Maximum Non-Repetitive Surge Current

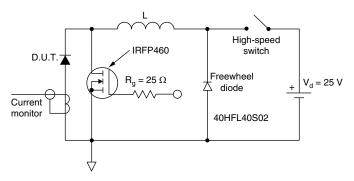


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

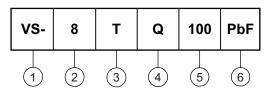
 $^{(1)}$  Formula used: T<sub>C</sub> = T<sub>J</sub> - (Pd + Pd<sub>REV</sub>) x R<sub>thJC</sub>; Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

## VS-8TQ...PbF Series, VS-8TQ...-N3 Series

Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

Device code



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

- Package:

T = TO-220

Schottky "Q" series

060 = 60 V 080 = 80 V

<u>5</u> - V

Voltage ratings —

100 = 100 V

6 - Environmental digit

100 - 100 V

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-8TQ060PbF	50	1000	Antistatic plastic tube				
VS-8TQ060-N3	50	1000	Antistatic plastic tube				
VS-8TQ080PbF	50	1000	Antistatic plastic tube				
VS-8TQ080-N3	50	1000	Antistatic plastic tube				
VS-8TQ100PbF	50	1000	Antistatic plastic tube				
VS-8TQ100-N3	50	1000	Antistatic plastic tube				

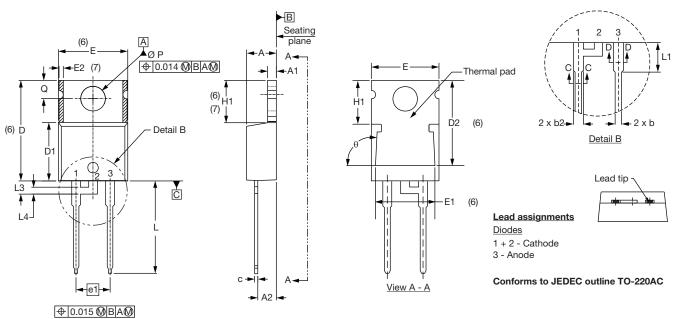
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95221</u>					
Dort marking information	TO-220AC PbF	www.vishay.com/doc?95224			
Part marking information	TO-220AC -N3	www.vishay.com/doc?95068			



### Vishay Semiconductors

### **TO-220AC**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	INCHES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6
Е	10.11	10.51	0.398	0.414	3, 6

SYMBOL	MILLIM	IETERS	INCHES		NOTES
STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
E1	6.86	8.89	0.270	0.350	6
E2	-	0.76	-	0.030	7
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6, 7
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
L3	1.78	2.13	0.070	0.084	
L4	0.76	1.27	0.030	0.050	2
ØΡ	3.54	3.73	0.139	0.147	
Q	2.60	3.00	0.102	0.118	
θ	90° t	o 93°	90° t	o 93°	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimension: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1
- (7) Dimension E2 x H1 define a zone where stamping and singulation irregularities are allowed
- (8) Outline conforms to JEDEC TO-220, D2 (minimum) where dimensions are derived from the actual package outline



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## Vishay:

<u>VS-8TQ060PBF</u> <u>VS-8TQ080GPBF</u> <u>8TQ060</u> <u>8TQ080</u> <u>8TQ080S</u> <u>8TQ080STRL</u> <u>8TQ080STRR</u> <u>8TQ100S</u> 8TQ100STRL 8TQ100STRR VS-8TQ080PBF VS-8TQ100PBF VS-8TQ100 VS-8TQ080-N3