PREPARED BY: DATE SHARP APPROVED BY: DATE ISSUE: 14-Oct-10 DISPLAY DEVICE BUSINESS GROUP PAGE: 23pages SHARP CORPORATION APPLICABLE GROUP DISPLAY DEVICE BUSINESS GROUP **SPECIFICATION** REVISION: 15-Feb-12 **DEVICE SPECIFICATION FOR TFT-LCD Module** MODEL No. LQ121S1LG81 These parts have corresponded with the RoHS ☐ CUSTOMER'S APPROVAL BY J.Kawanish/ GENERAL MANAGER ENGINEERING DEPARTMENT I DISPLAY DEVICE DIVISION II DISPLAY DEVICE BUSINESS GROUP SHARP CORPORATION

RECORDS OF REVISION

Model No.: LQ121S1LG81

Widdel No	LGIZIOIL				
SPEC No.	DATE	REVI SED No	PAGE	SUMMARY	NOTE
LD-22X03A	2010/10/14	110		-	
LD-22X03B	2010/11/24	Δ1	P.2	Error correction	
				(\times) 26 million colors \Rightarrow (O) 262,144 colors	
			P.8	Error correction	
				(×) Duty 0% : Min. Luminance ⇒ (○) Duty 10%	
LD-22X03C	2011/1/12	Δ2	P.7	0ms < t2 < 20ms ⇒ 20ms < t2 < 200ms	
LD-22X03D	2011/1/28	Δ3	P.9	"Negative" is added to remarks of "Vsync/Hsync cycle".	
			P.9	[Note1] is added.	
LD-22X03E	2011/3/11	△4	P.10	Positive timing of Vsync/Hsync is added.	
LD-22X03F	2012/2/15	△5	P.6	Error correction	
				(×)The VCC power ··· ⇒(O)The VDD power ···	
			P.16	(non-operating) is added to vibration test and shock test.	

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LD-22X03F- 1

1. Application

This specification applies to the color TFT-LCD module LQ121S1LG81.

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In case of using the device for applications such as control and safety equipment for transportation (controls of aircraft, trains, automobiles, etc.), rescue and security equipment and various safety related equipment which require higher reliability and safety, take into consideration that appropriate measures such as fail-safe functions and redundant system design should be taken.

Do not use the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment (trunk lines), nuclear power control equipment and medical or other equipment for life support.

SHARP assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in this specification.

Contact and consult with a SHARP sales representative for any questions about this device.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a White-LED Backlight unit. Graphics and texts can be displayed on a 800 × RGB × 600dots panel with about 262144 colors colors by using LVDS (Low Voltage Differential Signaling) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and +12.0V DC supply voltage for backlight.

The TFT-LCD panel used for this module is a high-brightness and high-contrast image.

The maximum viewing angle is in the 6o'clock direction.

The 12o'clock direction is difficult to reverse the grayscale.

The LED driver circuit is built into the module.

3. Mechanical technical literatures

Parameter	technical literatures	Unit
Display size	31 (12.1inch) Diagonal	cm
Active area	246.0(H) × 184.5(V)	mm
Divel former	800 (H) × 600 (V)	missal.
Pixel format	(1pixel=R+G+B dot)	pixel
Aspect ratio	4:3	
Pixel pitch	0.3075(H)×0.3075(V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally white	
Unit outline dimensions (*1)	276.0(W) × 209.0(H) × 9.1(D)	mm
Mass	600	g
Surface treatment	Anti-glare and hard-coating 3H	

^(*1) excluding the area of the connector cover.

Outline dimensions are shown in Fig.1.

4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V power supply)

Using connectors: FI-SEB20P (Japan Aviation Electronics industry Co., Ltd.)

Corresponding connectors: FI-SE20M or FI-S20S

(Japan Aviation Electronics industry Co., Ltd.)

Using LVDS receiver: Building into cotroll IC(THC63LVDF84B(Thine electronics) or Compatible product)

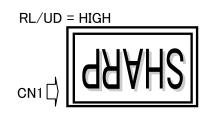
Corresponding LVDS transmitter: THC63LVDM83R (Thine electronics) or Compatible product

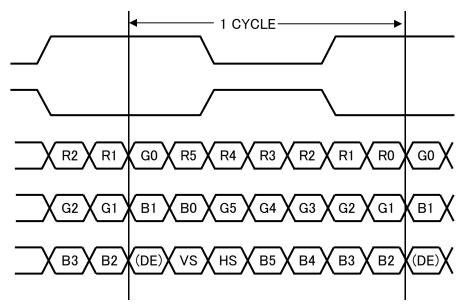
CN1

Pin	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND	GND	
4	GND	GND	
5	RxIN0-	LVDS receiver signal CH0 (-)	LVDS
6	RxIN0+	LVDS receiver signal CH0 (+)	LVDS
7	GND	GND	
8	RxIN1-	LVDS receiver signal CH1 (-)	LVDS
9	RxIN1+	LVDS receiver signal CH1 (+)	LVDS
10	GND	GND	
11	RxIN2-	LVDS receiver signal CH2 (-)	LVDS
12	RxIN2+	LVDS receiver signal CH2 (+)	LVDS
13	GND	GND	
14	CK IN-	LVDS receiver signal CK (-)	LVDS
15	CK IN+	LVDS receiver signal CK (+)	LVDS
16	GND	GND	
17	NC	Non Conection	
18	RL/UD		*1
19	GND	GND	
20	GND	GND	

[*1] RL/UD = LOW







HS: Hsync

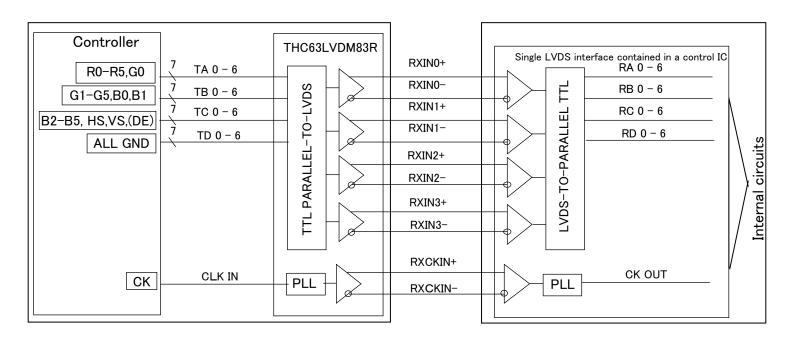
VS: Vsync

(DE:DATA ENABLE)

4-2. Interface block diagram

(Computer Side)

(TFT-LCD side)



4-3. LED backlight

LED backlight connector

CN2 Used connector : SM06B-SHLS-TF (J.S.T. Mfg. Co. Ltd)

Corresponding connector : SHLP-06V-S-B (J.S.T. Mfg. Co. Ltd)

Connector No.	Pin No.	symbol	function
	1	VDD	+12V power supply
	2	VDD	+12V power supply
CN2	3	GND	GND
GNZ	4	GND	GND
	5		Backlight ON/OFF signal
	6	VBR	PWM signal

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Pin	Ratings	Unit	Remark	
Supply voltage	pply voltage Vcc Ta=25°C VCC		VCC	−0.3 ~ +4.0	V	【*1,2】	
	V _{DD}	Ta=25°C	VDD	−0.3 ~ +15.0	٧	【*1,2】	
	V	Ta=25°C	RxINi-/+	00.1/00	٧	i=0,1,2	
Innut valtage	V _{I 1}	1a-25 C	CK IN-/+	-0.3∼Vcc+0.3	V	1-0,1,2	
Input voltage	V _{I 2}	Ta=25°C	RL/UD	-0.3∼Vcc+0.3	٧		
	V _{I 4}	Ta=25°C	XSTABY, VBR	-0.3 ~ +VDD	٧		
Storage temperature T _{STG} —		-	−30 ~ +80	°C	[*1]		
Operating temperature	T _{OPA}	_	-	−30 ~ +80	°C	[*1,3,4]	

[*1] Humidity:95%RH Max.(Ta≤40°C) Note static electricity.

Maximum wet-bulb temperature at 39°C or less. (Ta>40°C) No condensation.

- [*2] The VCC power supply capacity must use the one of 2A or more.
 - The VDD power supply capacity must use the one of 3A or more. $\Delta 5$
- [*3] There is a possibility of causing deterioration in the irregularity and others of the screen and the display fineness though the liquid crystal module doesn't arrive at destruction when using it at 65~80°C.

 There is a possibility of causing the fineness deterioration by the prolonged use in the (high temperature)

humidity environment (60% or more).

[*4] In the operating temperature item, the low temperature side is the ambient temperature regulations.

The high temperature side is the panel surface temperature regulations.

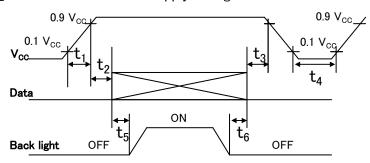
6. Electrical Characteristics

6-1. TFT-LCD panel driving

 $T_a = +25$ °C

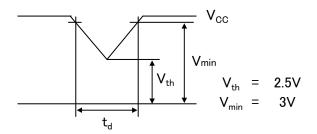
Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Supply voltag	;e	V _{cc}		3.0	3.3	3.6	V	[*1]
Current dissipa	tion	I_{CC}	Vcc=3.3V	_	270	350	mA	[*2]
Input voltage width for LVDS	S receiver	V_L		0	_	2.4	V	
Permissive input ripple	voltage	V_{RP}		_	_	200	mV_{P-P}	Vcc=3.3V
Differential input	High	V_{TH}		_	_	V _{CM} +100	mV	V _{CM} =+1.2V
Threshold voltage	Low	V_{TL}		V _{CM} -100	_	_	mV	[*3]
Township solling		V_{IH}		2.1	_	_	V	[*4]
Input voltage)	V_{IL}		_	_	0.8	V	
Input reak current		I_{OH}		_	_	400	μΑ	V ₁₂ =+3.3V [*4]
		I_{OL}		-10	_	+10	μΑ	V ₁₂ =0V [*4]
Terminal resist	tor	R _T			100	_	Ω	Differential input

[*1] On-off conditions for supply voltage



$$20 \, \mu \, \mathrm{s} \, < \, t_1 \, \stackrel{\textstyle \leq}{} \, 10 \mathrm{ms}$$
 $20 \mathrm{ms} \, < \, t_2 \, \stackrel{\textstyle \leq}{} \, 200 \mathrm{ms} \, \Delta 2$
 $0 \, < \, t_3 \, \stackrel{\textstyle \leq}{} \, 1 \mathrm{s}$
 $1 \mathrm{s} \, \stackrel{\textstyle \leq}{} \, t_4$
 $500 \mathrm{ms} \, \stackrel{\textstyle \leq}{} \, t_5$
 $200 \mathrm{ms} \, \stackrel{\textstyle \leq}{} \, t_6$

Vcc-dip conditions



- 1) Vth < V_{CC} \leq Vmin $t_d \leq$ 10ms
- 2) $V_{\rm CC}$ < $V_{\rm th}$

Vcc-dip conditions should also follow the On-off conditions for supply voltage

- ENAB need not be input so that this model may drive only by the Hsync/Vsync signal.
- The relation between the data input and the backlight lighting will recommend the above-mentioned input sequence. When the backlight is turned on before the panel operates, there is a possibility of abnormally displaying.

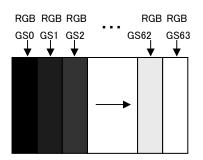
 The liquid crystal module is not damaged.

[*2] Current dissipation

Typical current situation : 64-gray-bar pattern (Vcc=+3.3V, fck = 40MHz, Ta=25°C)

[*3] V_{CM}: LVDS common mode voltage

[*4] RL/UD

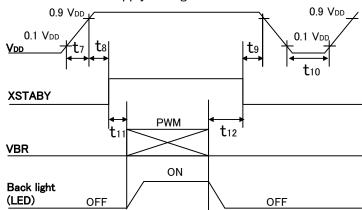


6-2. LED backlight

 $Ta=+25^{\circ}C$

Parameter		Symbol	Min.	Тур.	Max.	Unit	Remark
Supply	voltage	V _{DD}	10.2	12.0	13.8	V	[*1]
Command	dia ain atia n	I DD1	-	350	500	mA	【*2】
Gurrent	dissipation	I DD2	-	_	10	μΑ	
Permissive inp	ut ripple voltage	VRP_BL	1	1	200	mV _{P-P}	VDD=+12.0V
XSTABY	High voltage	VIH_BL1	2.4	1	VDD	V	【*3】
VOLVE	Low voltage	VIL_BL1	1	1	0.2	V	【*3】
VBR	High voltage	VIH_BL2	2.1	1	VDD	V	[*4]
VDK	Low voltage	VIL_BL2	1	1	0.8	V	[*4]
PWM fr	equency	fрwм	200	1	1K	Hz	【*4,5】
PWM duty Life time		Dрwм	10	-	100	%	【*4,5】
		L	-	(50,000) (Module)	-	h	【Reference】 【*6】

[*1] On-off conditions for supply voltage



 $20 \,\mu\,\mathrm{s} \le t_7 \le 200 \mathrm{ms}$

0ms ≦ t8

 $0 \text{ms} \leq t_9$

200ms ≤ t10

10ms ≦ t11

0ms ≦ t12

[*2] Current dissipation

Typ. value: VDD=+12.0V, Duty=100% Max. value: VDD=+10.2V, Duty=100%

[*3] Backlight ON/OFF signal (connected by the pull-down resistor of $10k\Omega$)

[*4] PWM signal (connected by the pull-down resistor of $10k\Omega$)

of $10k\Omega$) VBR t_{13} t_{14}

[*5] PWM

 $f_{PWM} = 1/t_{14}$

Duty 10%: Min. Luminance(0%: LED OFF) △1

Duty 100%: Max. Luminance

Luminance changes in proportion to the duty ratio. (t13 \ge 10 μ s)

When the frequency slows, the display fineness might decrease.

7. Timing characteristics of input signals

7-1. Timing characteristics

(Vertical Timing)

	Item	Symbol	min	typ	max	unit	remark	
	Vavra avala	TVA	-	17.6	_	ms	Negative	∆3
	Vsync cycle	IVA	628	666	798	line		
	Blanking period	TVB	28	66	-	line		
Vertical	Vsync pulse width	TVC	2	4	6	line		
vertical	Back porch	TVD	23	23	23	line		
	Vsync pulse width +Back porch	TVC+TVD	25	27	29	line		
	Active display area	TVE	600	600	600	line		
	Front porch	TVF	3	39	-	line		

(Horizontal Timing)

	Item	Symbol	min	typ	max	unit	remark	
	Hauma avala	T 11A	21	26.4	40	μs	Negative	△3
	Hsync cycle	THA	832	1056	1395	clock		
	Blanking period	THB	40	256	-	clock		
Horizontal	Hsync pulse width	THC	2	128	200	clock		
	Back porch	THD	88	88	88	clock		
	Active display area	THE	800	800	800	clock		
	Front porch	THF	0	40	_	clock		

(Clock Timing)

	Item	Symbol	min	typ	max	unit	remark
Clock	Frequency	1/Tc	35	40	42	MHz	[Note1]

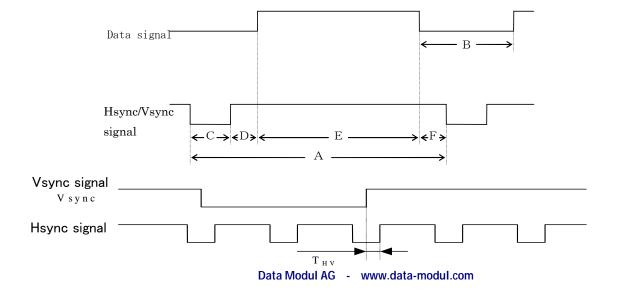
[Note1] In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred. $\triangle 3$

(Hsync-Vsync Phase difference)

(risylle vsylle) has difference,								
Item	Symbol	min	typ	max	unit	remark		
Hsync-Vsync Phase difference	THV	1	_	THA-THC	clock			

(Display position)

	Item Symbol		start	end	unit	remark
Vertical	rising edge of Vsync		24	623	line	
Horizontal	rising edge of	Hsync	89	888	clock	



XVsync, Hsync = Positive $\triangle 4$

(Note: Please do not put a noise into Hsync and Vsync.)

(Vertical Timing)

	Item	Symbol	min	typ	max	unit	remark
	\/a.waa a.uala	T) / A	_	17.6	-	ms	Positive
	Vsync cycle	TVA	628	666	798	line	
	Blanking period	TVB	28	66	-	line	
Vertical	Vsync pulse width	TVC	2	-	_	line	
	Back porch	TVD	23-TVC	23-TVC	23-TVC	line	
	Active display area	TVE	600	600	600	line	
	Front porch	TVF	3	43	_	line	

(Horizontal Timing)

	Item	Symbol	min	typ	max	unit	remark
	Hayraa ayrala	THA	21	26.4	40	μs	Positive
	Hsync cycle	ITA	832	1056	1395	clock	
	Blanking period	THB	40	256	-	clock	
Horizontal	Hsync pulse width	THC	2	-	-	clock	
	Back porch	THD	88-THC	88-THC	88-THC	clock	
	Active display area	THE	800	800	800	clock	
	Front porch	THF	0	168	_	clock	

(Clock Timing)

	Item	Symbol	min	typ	max	unit	remark
Clock	Frequency	1/Tc	35	40	42	MHz	[Note1]

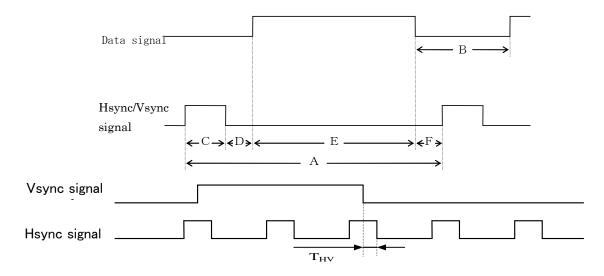
[Note1] In case of lower frequency, the deterioration of display quality, flicker etc., may be occurred.

(Hsync-Vsync Phase difference)

Item	Symbol	min	typ	max	unit	remark
Hsync-Vsync Phase difference	THV	1	_	THA-THC	clock	

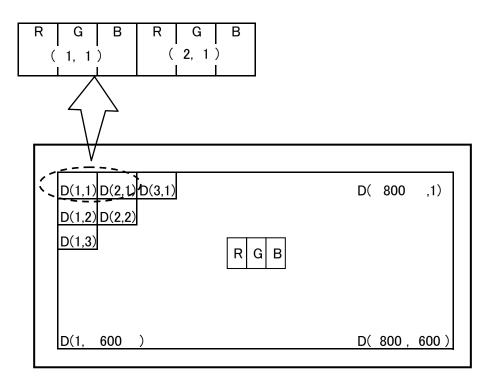
(Display position)

	Item Symbol		start	end	unit	remark
Vertical	rising edge of Vsync		24	623	line	
Horizontal	al rising edge of Hsync		89	888	clock	



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7-2. Input Data Signals and Display Position on the screen



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Colors &								D	ata	sigr	nal								
	Gray scale	GrayScale	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	В1	B2	ВЗ	В4	B5
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
o.	Green	_	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Col	Cyan	_	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
Basic Color	Red	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
В	Magenta	_	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Ф	1	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
f Re	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale o	1	\downarrow			,	l					,	l					,	l		
Gray Scale of Red	\downarrow	1	1			<u> </u>			↓											
Gray	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	\downarrow	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
en	1	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
Scale of Green	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
e of	1	1			,	l					,	l					,	l		
Scal	1	\downarrow			,	l					,	l					,	l		
Gray	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
G	1	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
	Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ō	1	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
f Blu	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
Gray Scale of Blue	1	\downarrow			`	l					,	l					,	l		
Sca	1	\downarrow		,	,	l					,	l	,				,	l		
Gray	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	1	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
	Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Each basic color can be displayed in 64 gray scales from 6 bit data signals. According to the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

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9. Optical Characteristics

Ta=+25°C, Vcc=+3.3V

Parar	meter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21, θ 22		60	80	_	Deg.	
angle		θ11	CR>10	35	60	-	Deg.	【*1,2,4】
range	vertical	<i>θ</i> 12		60	80	-	Deg.	
Contra	st ratio	CR	optimized angle	500	800	-		【*2,4】
Response Time	White Black	τr+τd		-	30	-	ms	【*3,4】
Chroma	ticity of	Wx		0.255	0.305	0.355		
Wh	nite	Wy		0.275	0.325	0.375		
Chroma	ticity of	Rx		_	0.560	-		
R	ed	Ry		_	0.330	-		F. 14.4.3
Chroma	ticity of	Gx	<i>θ</i> =0°	-	0.335	-		【*4】
Gre	een	Gy	0 -0	_	0.595	-		
Chroma	ticity of	Bx		-	0.155	-		
Blue		Ву		_	0.115	-		
Luminanc	e of white	Y _{L1}		350	450	-	cd/m²	[*4]
White U	niformity			_	_	1.33		【*5】

XThe measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.

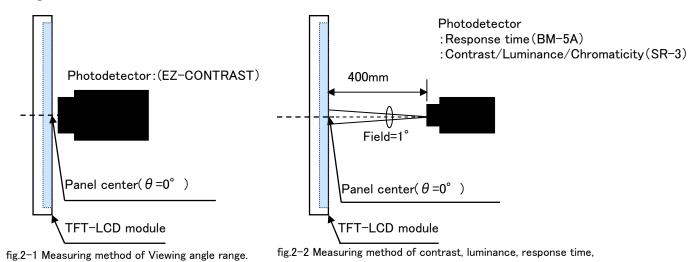
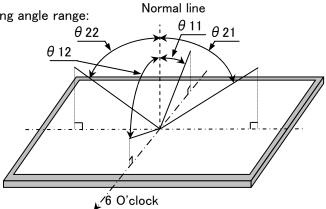


Fig.2 Optical characteristics measurement method

and Chromaticity.

LD-22X03F- 14

[*1] Definitions of viewing angle range:



[*2] Definition of contrast ratio:

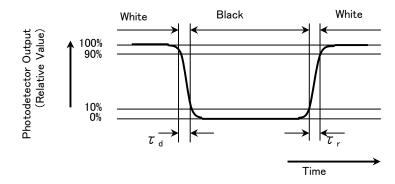
The contrast ratio is defined as the following. Contrast (CR) =

Luminance with all pixels white

Luminance with all pixels black

[*3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

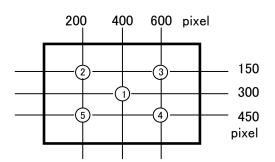




[*5] Definition of white uniformity:

White uniformity is defined as the following with five measurements. $(\widehat{1}) \sim \widehat{(5)}$

$$\delta_{\rm w} = \frac{{\rm Maximum\ luminance\ of\ 5\ points(1) \sim 5)}.}{{\rm Maximum\ luminance\ of\ 5\ points(1) \sim 5)}.}$$



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Since the front polarizer is easily damaged, pay attention not to scratch it.
- c) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- d) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- e) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- f) Since CMOS LSI is used in this module, take care of static electricity and injure the human earth when handling. Observe all other precautionary requirements in handling components.
- g) Since there is a circuit board in the module back, stress is not added at the time of a design assembly. Please make it like. If stress is added, there is a possibility that circuit parts may be damaged.
- h) It causes an irregular display and the defective indication, etc., when always put constant pressure on the back of the module.
 - Please do not make the structure to press the back of the module.
- i) Do not expose the LCD panel to direct sunlight. Lightproof shade etc. should be attached when LCD panel is used under such environment.
- j) Connect GND to stabilize against EMI and external noise.
- k) When handling LCD modules and assembling them into cabinets, please avoid that long-terms storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the modules. Do not use the LCD module under such environment.
- I) When the module is installed, please take care not to pull and to hang LED_FFC.
- m) Liquid crystal contained in the panel may leak if the LCD is broken. Rinse it as soon as possible if it gets inside your eye or mouth by mistake.
- n) Be careful when using it for long time with fixed pattern display as it may cause accidential image.
- o) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- p) If a minute particle enters in the module and adheres to an optical material, it may cause display non-uniformity issue, etc. Therefore, fine-pitch filters have to be installed to cooling and inhalation hole if you intend to install a fan.
- q) An abnormal display by changing in quality of the polarizing plate might occur regardless of contact or no contact to the polarizing plate, because of epoxy resin (amine system curing agent) that comes out from the material and the packaging material used for the set side, the silicon adhesive (dealcoholization system and oxime system), and the tray blowing agents (azo-compound), etc. Please confirm adaptability with your employed material.
- r) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.
- s) Notice: Never take to pieces the module, because it will cause failure. Please do not peel off the Black tape pasted to the product.
- t) When install LCD modules in the cabinet, please tighten with "torque=0.294±0.02N·m(3.0±0.2kgf·cm)".

11. Packing form

MAX. 6 a) Piling number of cartons :

b) Package quantity in one carton: 20pcs

c) Carton size(TYP): 517mm(W) × 432mm(D) × 347mm(H)

d) Total mass of one carton filled with full modules(20pcs): 14kg

12. Reliability test items

	No.	Test item	Conditions	Remark
	1	High temperature storage test	Ambient temperature 80°C 240H	[Note1]
	2	Low temperature strage test	Ambient temperature −30°C 240H	[Note1]
	3	High temperature & high humidity operation test	Ambient temperature 40°C, Humidity 95% RH 240H (No condensation.)	【Note1】
	4	High temperature operation test	Panel surface 80°C 240H	[Note1]
	5	Low temperature operation test	Ambient temperature −30°C 240H	[Note1]
△5	6	Vibration test (non-operating)	<pre><sin wave=""> Frequency :10~57Hz/Vibration width (one side) :0.076mm</sin></pre>	【Note1】
△5	7	Shock test (non-operating)	Max. gravity:490m/s2 Pulse width:11ms Direction:±X,±Y,±Z Test period:1time ✓1direction	[Note1]
	8	Thermal shock test	-30°C[0.5h]~80°C[0.5h]∕50cycles	[Note1]

[Note1] Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function. (normal operation state: Temperature:15~35°C, Humidity:45~75%, Atmospheric pressure:86~106kpa)

13. Others

13-1. Lot No Label:

A) Module serial label

The label that displays SHARP·Model No. (LQ121S1LG81)·Lot No. is stuck on the back of the module.

SHARP
LQ121S1LG81

Bar Code(Lot No.)

Lot No display method (Figure and alphabet)

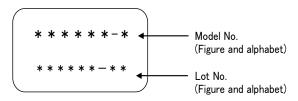
Production year
(Last digit of dominical year)

Production month (1-9X, Y, Z)

Assembly site code
Discernment code

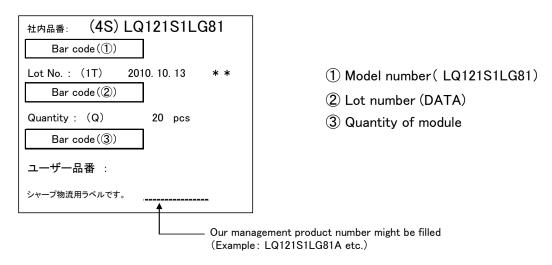
B) Backlight serial label

The label that displays the model No. and lot No. for the backlight is stuck on the back of the module.



13-2. Packing box Label:

The label that displays ①Model number(LQ121S1LG81) ②Lot number ③Quantity of module is stuck on the packing box. Moreover, the display of bar code also applies to this.



A right picture is written to the packing box of module for the RoHS restriction.

X R.C. (RoHs Compliance) means these parts have corresponded with the RoHs directive. This module corresponds from the first sample to RoHS Directive.

R.C.

The figure below is written under the SHARP logo of the packing box about the production country.



- 13-3. The ozone-depleting substances is not used.
- 13-4. If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

14. Storage conditions

Environmental condition range of storage temperature and humidity

Temperature 0 to 40 degrees Celsius

Relative humidity 95% and below

[Note] Please refer below as a mean value of the environmental conditions.

Summer time temperature 20 to 35 degrees Celsius humidity, 85% and below

Winter time temperature 5 to 15 degrees Celsius humidity, 85% and below

Please maintain within 240 hours of accumulated length of storage time, with conditions of 40 degrees Celsius and room humidity of 95%.

Direct sun light

Please keep the product in a dark room or cover the product to protect from direct sun light.

Atmospheric condition

Please refrain from keeping the product with possible corrosive gas or volatile flux.

Prevention of dew

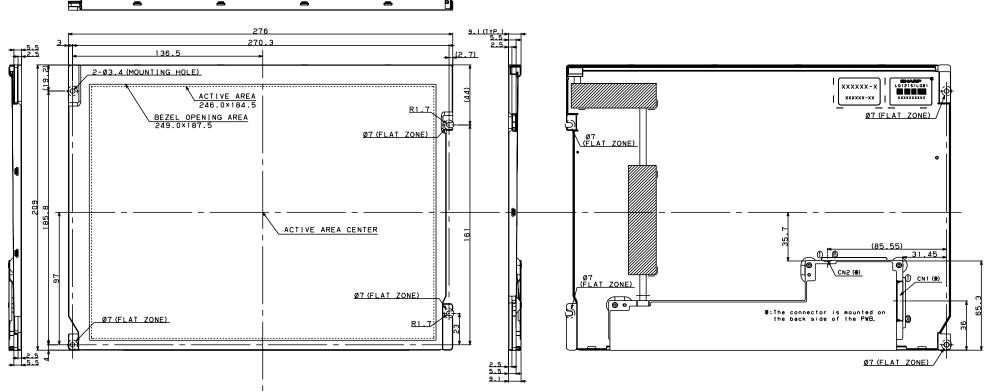
Please store the product carton either on a wooden pallet or a stand / rack to prevent dew.

Do not place directly on the floor. In addition, to obtain moderate ventilation in between the pallet's top and bottom surfaces, pile the cartons up in a single direction and in order.

Please place the product cartons away from the storage wall.

Storage period

Within above mentioned conditions, maximum storage period should be one year.



[I/F Connector]

CN1:FI-SEB20P (JAE)

PIN LAYOUT

1	2	3	4	5	6	7	8
VCC	VCC	GND	GND	RXINO-	RXIN0+	GND	RXIN1-
9	10	1.1	12	13	14	15	16
RXIN1+	GND	RXIN2-	RXIN2+	GND	CKIN-	CKIN+	GND
17	18	19	20				
NC	RL/UD	GND	GND				
				-			

[LED Backlight Connector]
 cn2:sm06B-shls-tf(JST)

PIN I AYOUT

1 114 [71	TN EATOOT										
1	2	3	4	5	6						
VDD	VDD	GND	GND	XSTABY	VBR						

BEZEL/DISPLAY POSITION

NOTES

1) UNSPECIFIED TOLERANCE TO BE ±0.5

3) TIGHTEN TORQUE (RECOMMENDATION)

THE SCREW IN A REAL MACHINE.

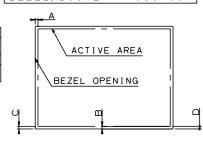
4) NEVER TAKE TO PIECES THE MODULE,
BECAUSE IT WILL CAUSE FAILURE.

 $: 0.294\pm0.02N \cdot m (3.0\pm0.2kgf \cdot cm)$

*PLASE CONFIRM WHETHER THERE IS
THE PROBLEM SUCH AS LOOSENING OF

PLEASE DO NOT PELL OFF THE PWB COVER, SCREW, TAPE PASTED TO THE PRODUCT.

2) WARP AND FLOATING FOR PWB COVER.FFC.CHASSIS ARE EXCLUDED FROM THE THICKNESS OF THE UNIT.



- 1) TOLERANCE X-DIRECTION A:1.5±0.8
- 2) TOLERANCE Y-DIRECTION B:1.5±0.8
- 3) OBLIQUITY OF DISPLAY AREA IC-DI<0.8

Fig.1: LQ121S1LG81 OUTLINE

Data Modul AG - www.data-modul.com

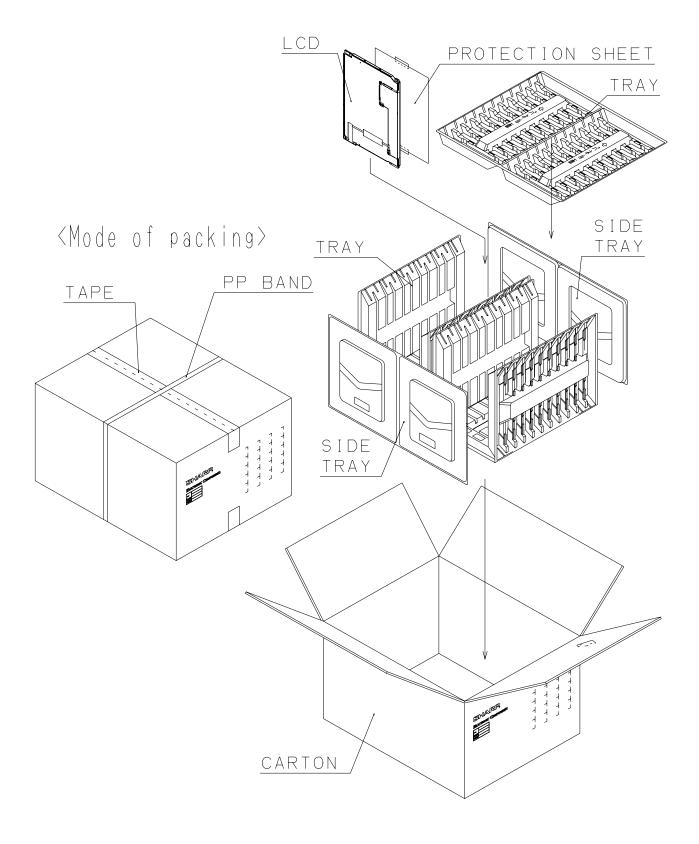


Fig 3: PACKING FORM