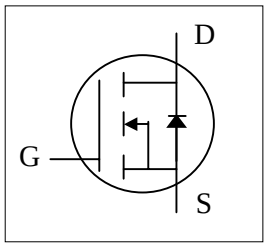
Advanced Power Electronics Corp.

AP85T03GH/J

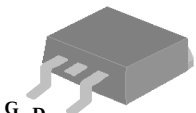
RoHS-compliant Product

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

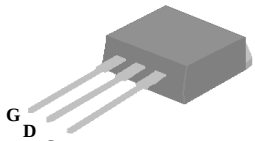
- ▼ **Low Gate Charge**
- ▼ **Simple Drive Requirement**
- ▼ **Fast Switching**



BV_{DSS}	30V
$R_{DS(ON)}$	6mΩ
I_D	75A



TO-252(H)



TO-251(J)

Description

The TO-252 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP85T03GJ) is available for low-profile applications.

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V	75	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	55	A
I_{DM}	Pulsed Drain Current[1]	350	A
P_D@T_C=25℃	Total Power Dissipation	107	W
	Linear Derating Factor	0.7	W/℃
T_{STG}	Storage Temperature Range	-55 to 175	℃
T_J	Operating Junction Temperature Range	-55 to 175	℃

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	1.4	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	30	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25℃, I_D=1mA	-	0.02	-	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=45A	-	-	6	mΩ
		V_{GS}=4.5V, I_D=30A	-	-	10	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	1	-	3	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=30A	-	55	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=30V, V_{GS}=0V	-	-	1	uA
	Drain-Source Leakage Current (T_j=175°C)	V_{DS}=24V, V_{GS}=0V	-	-	500	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±20V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=30A	-	33	52	nC
Q_{gs}	Gate-Source Charge	V_{DS}=24V	-	8		nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=4.5V	-	24		nC
Q_{oss}	Output Charge	V_{DD}=15V,V_{GS}=0V	-	24.5	39	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=15V	-	11	-	ns
t_r	Rise Time	I_D=30A	-	77	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω,V_{GS}=10V	-	35	-	ns
t_f	Fall Time	R_D=0.5Ω	-	67	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	2700	4200	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	550	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	380	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=45A, V_{GS}=0V	-	-	1.3	V
t_{rr}	Reverse Recovery Time[2]	I_S=30A, V_{GS}=0V,	-	28	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	10	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse width ≤300us , duty cycle ≤2%.

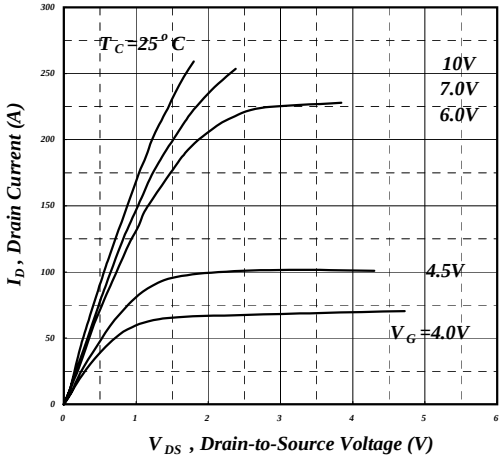


Fig 1. Typical Output Characteristics

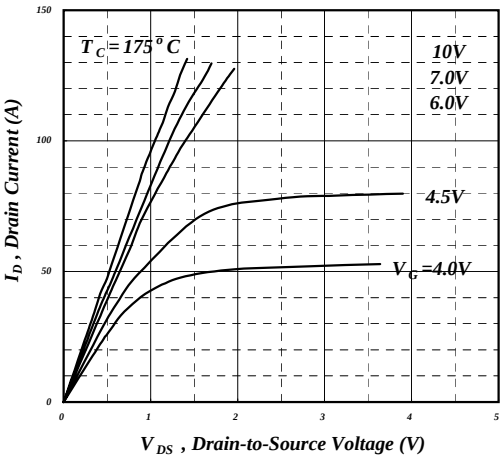


Fig 2. Typical Output Characteristics

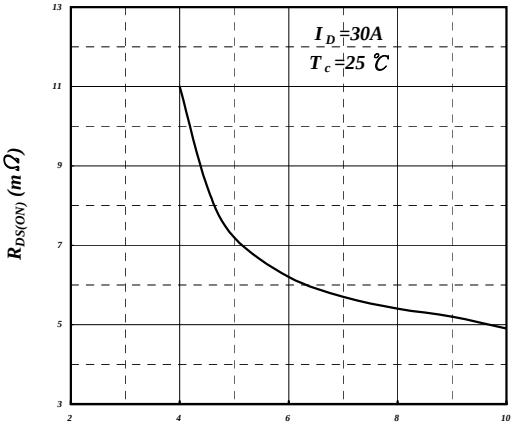


Fig 3. On-Resistance v.s. Gate Voltage

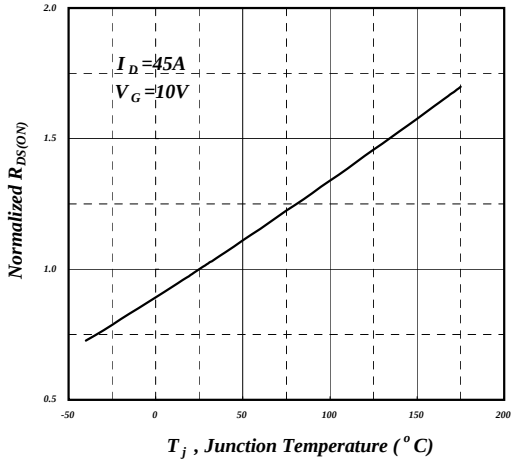


Fig 4. Normalized On-Resistance v.s. Junction Temperature

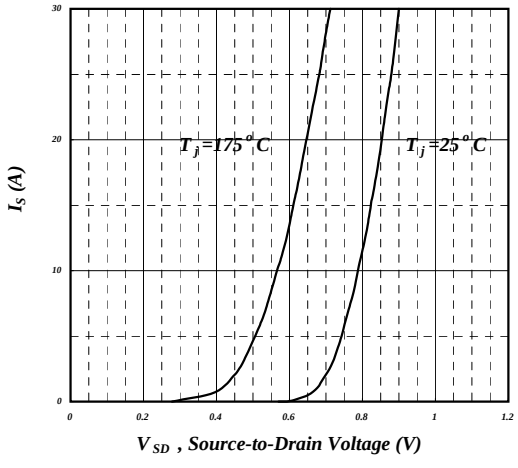


Fig 5. Forward Characteristic of Reverse Diode

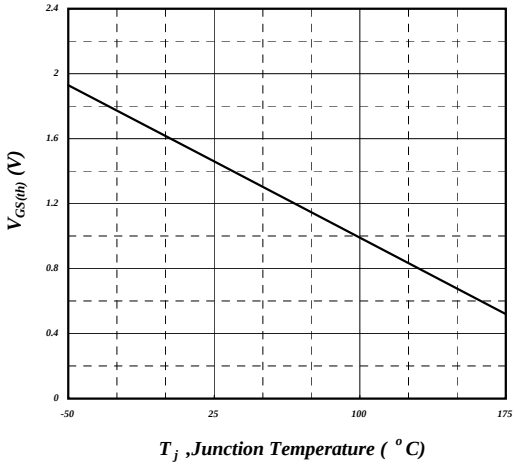


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

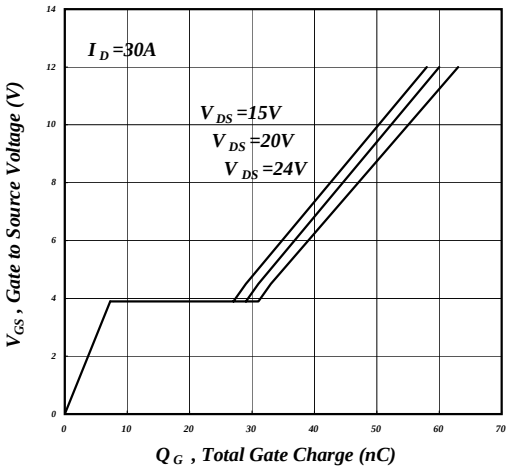


Fig 7. Gate Charge Characteristics

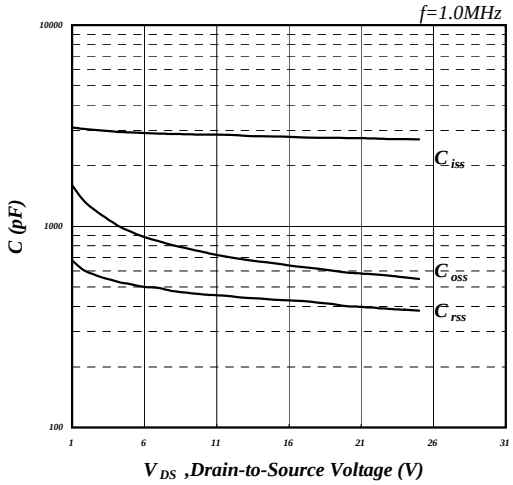


Fig 8. Typical Capacitance Characteristics

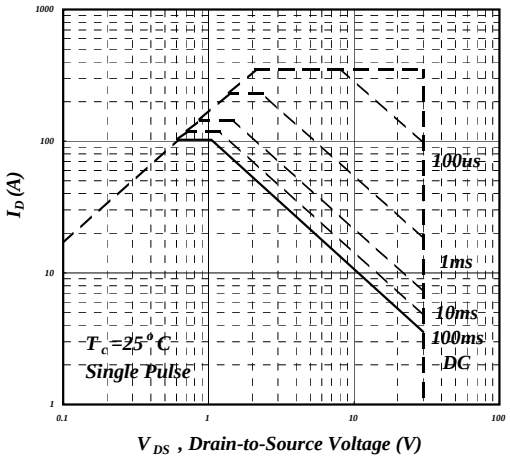


Fig 9. Maximum Safe Operating Area

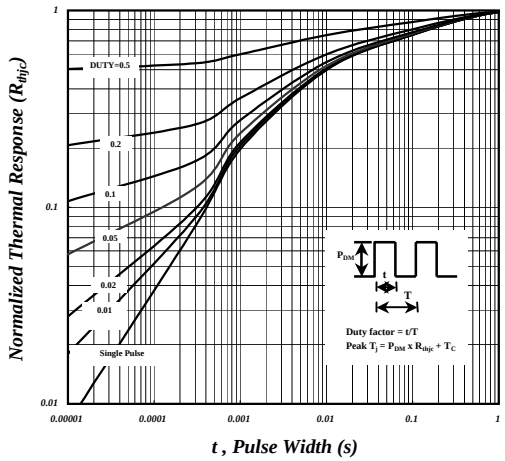


Fig 10. Effective Transient Thermal Impedance

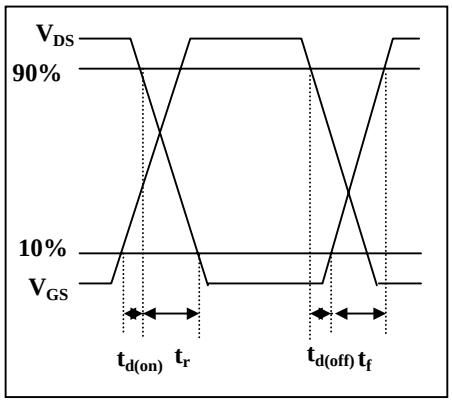


Fig 11. Switching Time Waveform

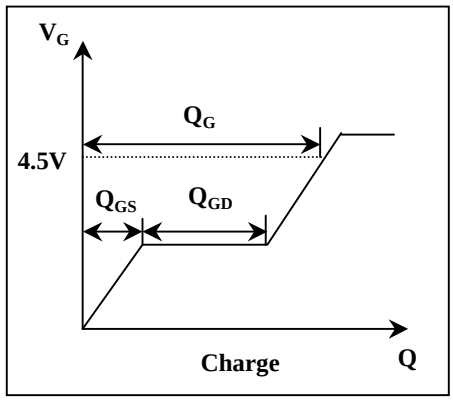
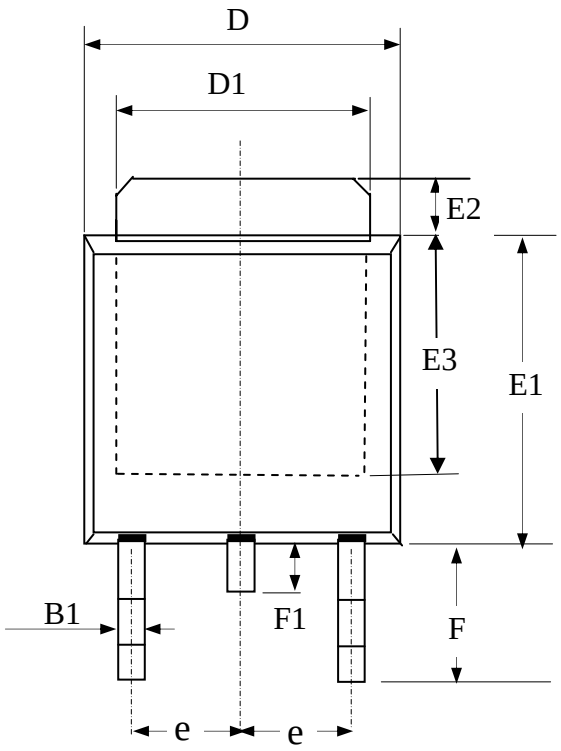


Fig 12. Gate Charge Waveform

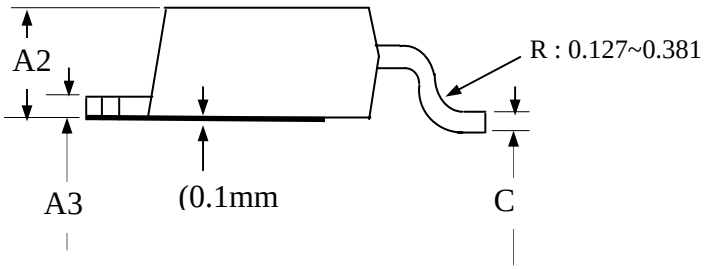
Package Outline : TO-252



SYMBOLS	Millimeters		
	MIN	NOM	MAX
A2	1.80	2.30	2.80
A3	0.40	0.50	0.60
B1	0.40	0.70	1.00
D	6.00	6.50	7.00
D1	4.80	5.35	5.90
E3	3.50	4.00	4.50
F	2.20	2.63	3.05
F1	0.50	0.85	1.20
E1	5.10	5.70	6.30
E2	0.50	1.10	1.80
e	--	2.30	--
C	0.35	0.50	0.65

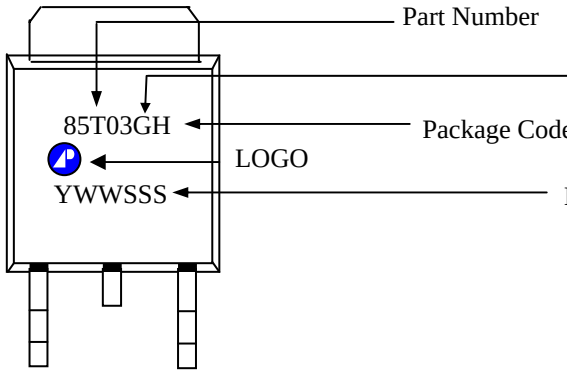
1.All Dimensions Are in Millimeters.

2.Dimension Does Not Include Mold Protrusions.



Part Marking Information & Packing : TO-252

Laser Marking

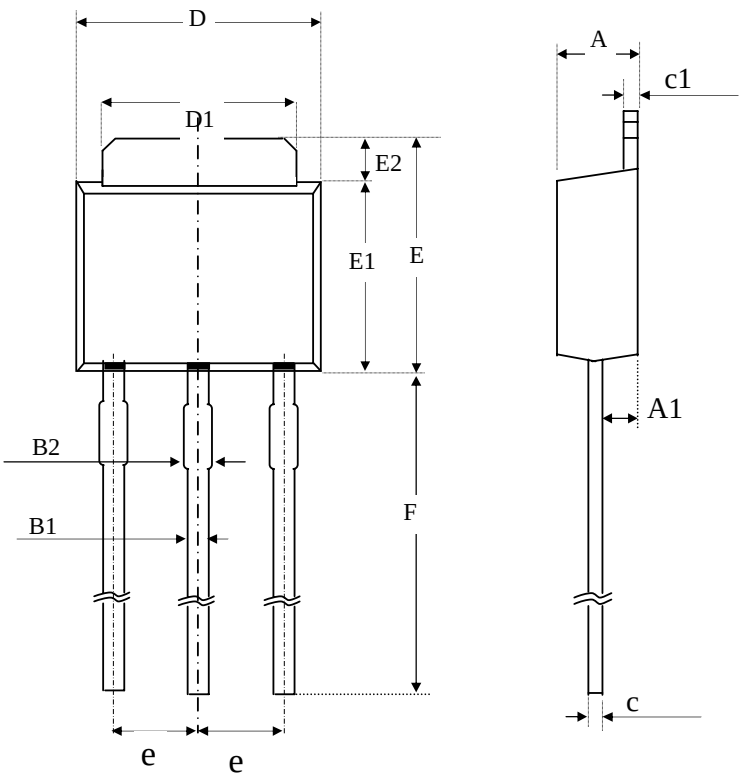


Y：Last Digit Of The Year

WW：Week

SSS：Sequence

Package Outline : TO-251

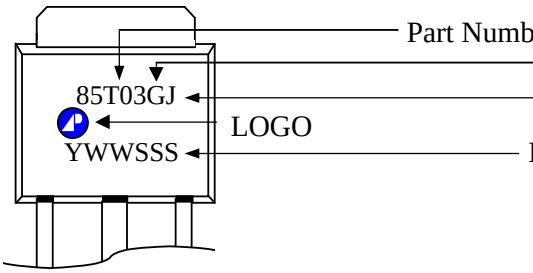


SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	2.20	2.30	2.40
A1	0.90	1.20	1.50
B1	0.40	0.60	0.80
B2	0.60	0.85	1.05
c	0.40	0.50	0.60
c1	0.40	0.50	0.60
D	6.40	6.60	6.80
D1	4.80	5.20	5.50
E	6.70	7.00	7.30
E1	5.40	5.60	5.80
E2	1.30	1.50	1.70
e	----	2.30	----
F	7.00	8.30	9.60

1.All Dimensions Are in Millimeters.

2.Dimension Does Not Include Mold Protrusions.

Part Marking Information & Packing : TO-251



Part Number

meet Rohs requirement
for low voltage MOSFET only

Package Code

LOGO

Date Code (YWWSSS)

Y ：Last Digit Of The Year

WW ：Week

SSS ：Sequence

If last "S" is numerical letter : Rohs product

If last "S" is English letter : HF & Rohs product